

# PMSTA92

PNP high-voltage transistor 16 May 2019

**Product data sheet** 

## 1. General description

PNP high-voltage transistor in a very small SOT323 (SC-70) Surface-Mounted Device (SMD) plastic package.

NPN complement: PMSTA42

## 2. Features and benefits

- Very small package
- High voltage
- AEC-Q101 qualified

## 3. Applications

• Primarily intended for use in telephony and professional communication equipment.

## 4. Quick reference data

Table 1. Quick reference data							
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V <sub>CEO</sub>	collector-emitter voltage	open base		-	-	-300	V
I <sub>C</sub>	collector current			-	-	-100	mA
h <sub>FE</sub>	DC current gain	V <sub>CE</sub> = -10 V; I <sub>C</sub> = -30 mA		30	-	-	

## 5. Pinning information

Table 2.	Pinning in	formation		
Pin	Symbol	Description	Simplified outline	Graphic symbol
1	В	base	3	Ç
2	E	emitter		вщ
3	С	collector		۲ <b>۲</b>
				E sym132
			1 2 SC-70 (SOT323)	
			SC-70 (SO1323)	



## 6. Ordering information

Table 3. Ordering information						
Type number         Package						
	Name	Description	Version			
PMSTA92	SC-70	plastic surface-mounted package; 3 leads	SOT323			

## 7. Marking

Table 4. Marking codes	
Type number	Marking code[1]
PMSTA92	%2D

[1] % = placeholder for manufacturing site code

## 8. Limiting values

#### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V <sub>CBO</sub>	collector-base voltage	open emitter		-	-300	V
V <sub>CEO</sub>	collector-emitter voltage	open base		-	-300	V
V <sub>EBO</sub>	emitter-base voltage	open collector		-	-5	V
I <sub>C</sub>	collector current			-	-100	mA
I <sub>CM</sub>	peak collector current	single pulse; t <sub>p</sub> ≤ 1 ms		-	-200	mA
I <sub>BM</sub>	peak base current			-	-100	mA
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C	[1]	-	200	mW
Tj	junction temperature			-	150	°C
T <sub>amb</sub>	ambient temperature			-65	150	°C
T <sub>stg</sub>	storage temperature			-65	150	°C

[1] Refer to SOT323 (SC-70) standard mounting conditions.

## 9. Thermal characteristics

Table 6. Thermal characteristics							
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
R <sub>th(j-a)</sub>	thermal resistance from junction to ambient	in free air	[1]	-	-	625	K/W

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

## **10. Characteristics**

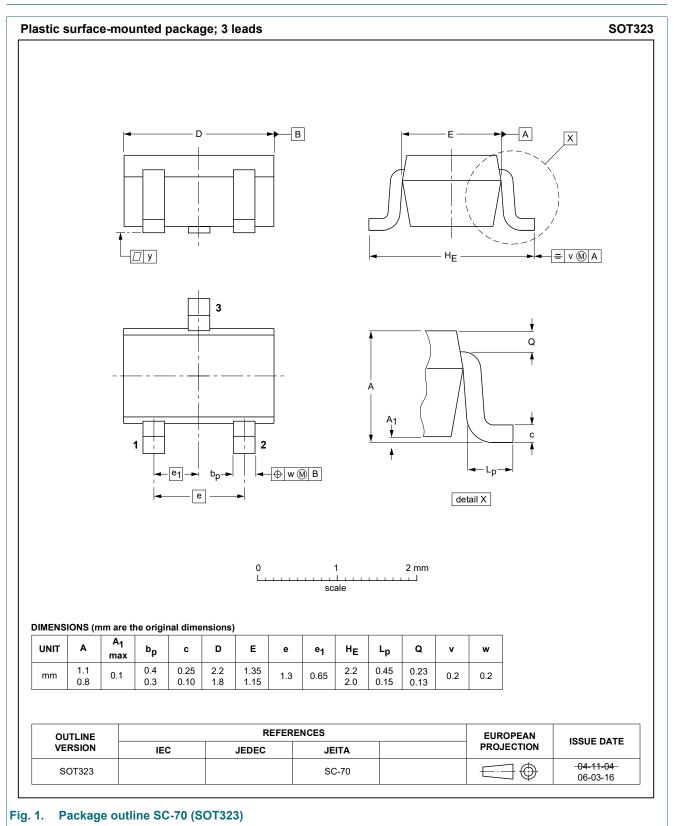
## Table 7. Characteristics

 $T_{amb}$  = 25 °C, unless otherwise specified

Symbol	Parameter	Conditions	Mi	n Typ	Max	Unit
I <sub>CBO</sub>	collector-base cut-off current	V <sub>CB</sub> = -200 V; I <sub>E</sub> = 0 A	-	-	-100	nA
I <sub>EBO</sub>	emitter-base cut-off current	V <sub>EB</sub> = -3 V; I <sub>C</sub> = 0 A	-	-	-100	nA
h <sub>FE</sub>	DC current gain	V <sub>CE</sub> = -10 V; I <sub>C</sub> = -1 mA	40	-	-	
		V <sub>CE</sub> = -10 V; I <sub>C</sub> = -10 mA	40	-	-	
		V <sub>CE</sub> = -10 V; I <sub>C</sub> = -30 mA	30	-	-	
V <sub>CEsat</sub>	collector-emitter saturation voltage	I <sub>C</sub> = -20 mA; I <sub>B</sub> = -2 mA; pulsed; t <sub>p</sub> ≤ 300 μs; δ ≤ 0.02; T <sub>amb</sub> = 25 °C	-	-	-250	mV
V <sub>BEsat</sub>	base-emitter saturation voltage		-	-	-900	mV
C <sub>c</sub>	collector capacitance	V <sub>CB</sub> = -20 V; I <sub>E</sub> = 0 A; i <sub>e</sub> = 0 A; f = 1 MHz	-	1.9	3.5	pF
C <sub>e</sub>	emitter capacitance	V <sub>EB</sub> = -5 V; I <sub>C</sub> = 0 A; i <sub>c</sub> = 0 A; f = 1 MHz	-	20	-	pF
f <sub>T</sub>	transition frequency	V <sub>CE</sub> = -20 V; I <sub>C</sub> = -10 mA; f = 100 MHz	50	-	-	MHz

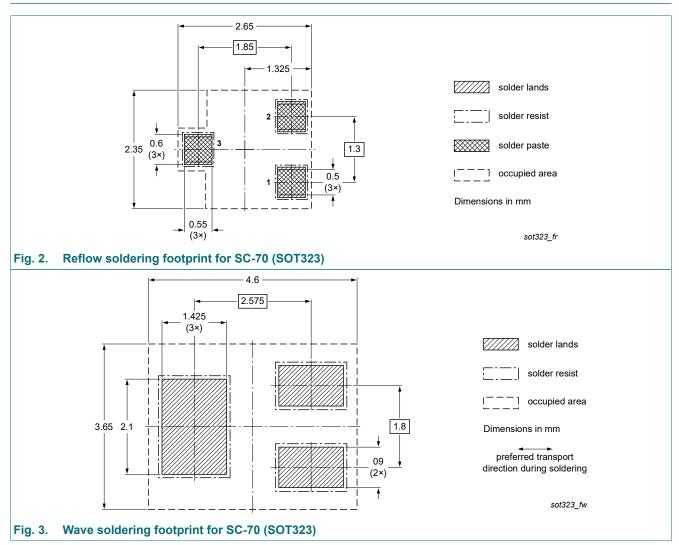
## **PNP** high-voltage transistor

## 11. Package outline



### **PNP** high-voltage transistor

## 12. Soldering



**Product data sheet** 

## **13. Revision history**

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PMSTA92 v.4	20190516	Product data sheet	-	PMSTA92 v.3
Modifications:	<ul> <li>The format of the Nexperia.</li> </ul>	ng code corrected nis data sheet has been rede e been adapted to the new c		
PMSTA92 v.3	20010220	Product data sheet	-	PMSTA92_93 v.2
PMSTA92 v.3 PMSTA92_93 v.2	20010220 19990601	Product data sheet Product data sheet	-	PMSTA92_93 v.2 PMSTA92_93 v.1

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# 14. Legal information

#### Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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- [2] The term 'short data sheet' is explained in section "Definitions".
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