**Product data sheet** 

## 1. General description

NPN/PNP low  $V_{\text{CEsat}}$  double transistor in a SOT666 ultra small and flat lead Surface-Mounted Device (SMD)plastic package.

### 2. Features and benefits

- 300 mW total power dissipation
- Very small 1.6 x 1.2 mm ultra thin package
- · Self alignment during soldering due to straight leads
- · Low collector capacitance
- Low V<sub>CFsat</sub>
- High current capabilities
- Improved thermal behaviour due to flat leads
- Reduced required PCB area
- Reduced pick and place costs.

## 3. Applications

- · Heavy duty battery powered equipment (telecom and audio-video) such as lamp drivers
- V<sub>CEsat</sub> critical applications such as latest low supply voltage IC applications
- · All battery driven equipment, to save battery power

### 4. Quick reference data

### Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit	
Per transistor; for the PNP transistor with negative polarity								
V <sub>CEO</sub>	collector-emitter voltage	open base		-	-	12	V	
I <sub>C</sub>	collector current			-	-	500	mA	
h <sub>FE</sub>	DC current gain	V <sub>CE</sub> = 2 V; I <sub>C</sub> = 10 mA; T <sub>amb</sub> = 25 °C		200	-	-		



# 5. Pinning information

#### **Table 2. Pinning information**

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	E1	emitter TR1	6 5 4	C1 B2 E2
2	B1	base TR1		
3	C2	collector TR2		(TR1 TR2)
4	E2	emitter TR2		
5	B2	base TR2	1 2 3	
6	C1	collector TR1	SOT666	sym019

# 6. Ordering information

#### **Table 3. Ordering information**

Type number	Package						
	Name	Description	Version				
PEMZ7	SOT666	plastic, surface-mounted package; 6 leads; 0.5 mm pitch; 1.6 mm x 1.2 mm x 0.55 mm body	SOT666				

## 7. Marking

### Table 4. Marking codes

Type number	Marking code
PEMZ7	<b>Z</b> 7

# 8. Limiting values

#### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
Per transist	or; for the PNP transistor wit	h negative polarity	'			
V <sub>CBO</sub>	collector-base voltage	open emitter		-	15	V
V <sub>CEO</sub>	collector-emitter voltage	open base		-	12	V
V <sub>EBO</sub>	emitter-base voltage	open collector		-	6	V
I <sub>C</sub>	collector current			-	500	mA
I <sub>CM</sub>	peak collector current			-	1	Α
I <sub>BM</sub>	peak base current			-	100	mA
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C	[1]	-	200	mW
Per device			'	'	'	'
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C	[1]	-	300	mW
Tj	junction temperature			-	150	°C
T <sub>amb</sub>	ambient temperature			-65	150	°C
T <sub>stg</sub>	storage temperature			-65	150	°C

<sup>[1]</sup> Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

## 9. Thermal characteristics

#### **Table 6. Thermal characteristics**

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
uiu-a)	thermal resistance from junction to ambient	in free air	[1] [2]	-	-	416	K/W

<sup>1]</sup> Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

## 10. Characteristics

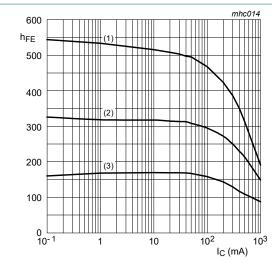
#### **Table 7. Characteristics**

Symbol	Parameter	Conditions	ı	Min	Тур	Max	Unit
Per transis	tor; for the PNP transistor	with negative polarity			'		'
I <sub>CBO</sub>	collector-base cut-off	V <sub>CB</sub> = 15 V; I <sub>E</sub> = 0 A; T <sub>amb</sub> = 25 °C	-	-	-	100	nA
	current	V <sub>CB</sub> = 15 V; I <sub>E</sub> = 0 A; T <sub>j</sub> = 150 °C	-		-	50	μA
I <sub>EBO</sub>	emitter-base cut-off current	V <sub>EB</sub> = 5 V; I <sub>C</sub> = 0 A; T <sub>amb</sub> = 25 °C	-	-	-	100	nA
h <sub>FE</sub>	DC current gain	V <sub>CE</sub> = 2 V; I <sub>C</sub> = 10 mA; T <sub>amb</sub> = 25 °C	2	200	-	-	
V <sub>CEsat</sub>	collector-emitter saturation voltage	$I_C$ = 200 mA; $I_B$ = 10 mA; pulsed; $t_p \le$ 300 μs; δ ≤ 0.02; $T_{amb}$ = 25 °C	-	-	-	220	mV
Transistor	1 (NPN)						
C <sub>c</sub>	collector capacitance	$V_{CB}$ = 10 V; $I_{E}$ = 0 A; $i_{e}$ = 0 A; $f$ = 1 MHz; $T_{amb}$ = 25 °C	-	•	4.4	6	pF
f <sub>T</sub>	transition frequency	V <sub>CE</sub> = 5 V; I <sub>C</sub> = 100 mA; f = 100 MHz; T <sub>amb</sub> = 25 °C	2	250	420	-	MHz
Transistor	2 (PNP)						1
C <sub>c</sub>	collector capacitance	V <sub>CB</sub> = -10 V; I <sub>E</sub> = 0 A; i <sub>e</sub> = 0 A; f = 1 MHz; T <sub>amb</sub> = 25 °C	-	-	-	10	pF
f <sub>T</sub>	transition frequency	$V_{CE}$ = -5 V; $I_{C}$ = -100 mA; f = 100 MHz; $T_{amb}$ = 25 °C	,	100	280	-	MHz

<sup>[2]</sup> Reflow soldering is the only recommended soldering method.

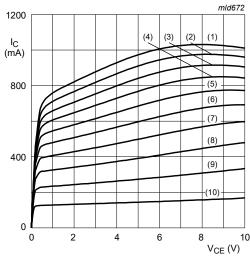
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#### NPN/PNP general purpose double transistor



V<sub>CE</sub> = 2 V (1) T<sub>amb</sub> = 150 °C (2) T<sub>amb</sub> = 25 °C (3) T<sub>amb</sub> = -55 °C

Fig. 1. TR1 (NPN): DC current gain as a function of collector current; typical values



 $T_{amb}$  = 25 °C

(1)  $I_B = 4.60 \text{ mA}$ 

(2)  $I_B = 4.14 \text{ mA}$ 

(3)  $I_B = 3.68 \text{ mA}$ (4)  $I_B = 3.22 \text{ mA}$ 

(5)  $I_B = 2.76 \text{ mA}$ 

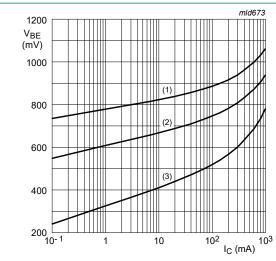
 $(6) I_B = 2.30 \text{ mA}$  $(7) I_B = 1.84 \text{ mA}$ 

(8)  $I_B = 1.38 \text{ mA}$ 

(9)  $I_B = 0.92 \text{ mA}$ 

 $(10) I_B = 0.46 \text{ mA}$ 

Fig. 2. TR1 (NPN): Collector current as a function of collector-emitter voltage; typical values



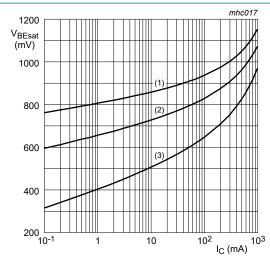
 $V_{CE} = 2 V$ 

(1)  $T_{amb} = -55 \, ^{\circ}C$ 

(2) T<sub>amb</sub> = 25 °C

(3) T<sub>amb</sub> = 150 °C

TR1 (NPN): Base-emitter voltage as a function Fig. 3. of collector current; typical values



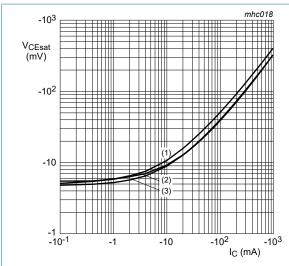
 $I_{\rm C}/I_{\rm B} = 20$ 

(1)  $T_{amb} = -55 \, ^{\circ}C$ 

(2)  $T_{amb} = 25 \, ^{\circ}C$ 

(3)  $T_{amb} = 150 \, ^{\circ}C$ 

Fig. 4. TR1 (NPN): Base-emitter saturation voltage as a function of collector current; typical values

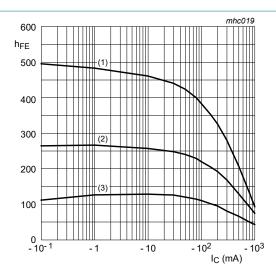


 $I_C/I_B = 20$ (1)  $T_{amb} = 150 \,^{\circ}C$ 

(2)  $T_{amb}$  = 25 °C

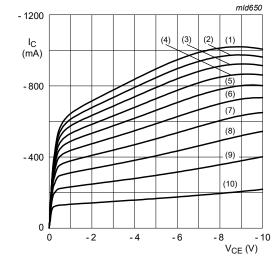
(3)  $T_{amb} = -55 \, ^{\circ}C$ 

Fig. 5. TR1 (NPN): Collector-emitter saturation voltage as a function of collector current; typical values



V<sub>CE</sub> = -2 V (1) T<sub>amb</sub> = 150 °C (2) T<sub>amb</sub> = 25 °C (3) T<sub>amb</sub> = -55 °C

Fig. 6. TR2 (PNP): DC current gain as a function of collector current; typical values



T<sub>amb</sub> = 25 °C

(1)  $I_B = -7.0 \text{ mA}$ 

(2)  $I_B = -6.3 \text{ mA}$ 

(3)  $I_B = -5.6 \text{ mA}$ (4)  $I_B = -4.9 \text{ mA}$ 

 $(5) I_B = -4.2 \text{ mA}$ 

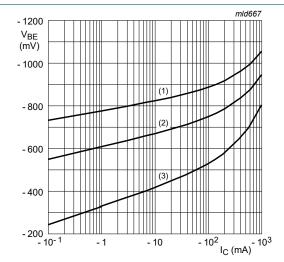
(6)  $I_B = -3.5 \text{ mA}$ 

 $(7) I_B = -2.8 \text{ mA}$ 

(8)  $I_B = -2.1 \text{ mA}$ 

(9)  $I_B = -1.4 \text{ mA}$ (10)  $I_B = -0.7 \text{ mA}$ 

TR2 (PNP): Collector current as a function of Fig. 7. collector-emitter voltage; typical values



 $V_{CE} = -2 V$ 

 $(1) T_{amb} = -55 °C$ 

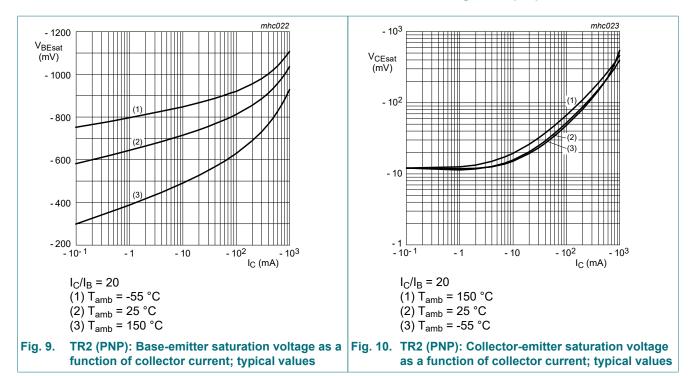
(2)  $T_{amb} = 25 \, ^{\circ}C$ 

(3)  $T_{amb} = 150 \, ^{\circ}C$ 

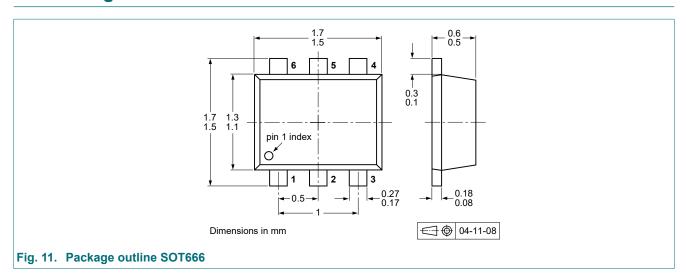
TR2 (PNP): Base-emitter voltage as a function Fig. 8. of collector current; typical values

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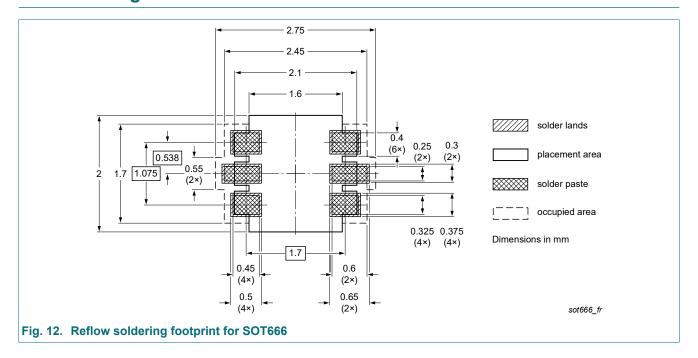
#### NPN/PNP general purpose double transistor



## 11. Package outline



# 12. Soldering



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# 13. Revision history

#### **Table 8. Revision history**

Table 6. Revision i	notor y						
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes			
PEMZ7 v.3	20221229	Product data sheet	-	PEMZ7 v.2			
Modifications:	<ul> <li>The format of this data sheet has been redesigned to comply with the identity guidelines of Nexperia.</li> <li>Legal texts have been adapted to the new company name where appropriate.</li> <li>Product(s) changed to non-automotive qualification.</li> </ul>						
PEMZ7 v.2	20011107	Product data sheet	-	PEMZ7 v.1			
PEMZ7 v.1	20010925	Product data sheet	-	-			

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#### **Data sheet status**

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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