

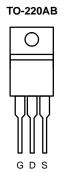
# N-Channel 60 V (D-S) MOSFET

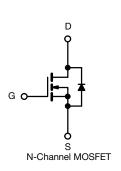
PRODUCT SUMMARY				
V <sub>DS</sub> (V)	$R_{DS(on)}(\Omega)$	I <sub>D</sub> (A) <sup>a</sup>		
60	$0.005$ at $V_{GS} = 10 \text{ V}$	120		
	$0.008$ at $V_{GS} = 7.5 \text{ V}$	100		

## **FEATURES**

- 175 °C Junction Temperature
- TrenchFET® Power MOSFET
- Material categorization:







Parameter		Symbol	Limit	Unit	
Gate-Source Voltage		V <sub>GS</sub>	± 20	V	
Continuous Daris Comment /T 475 90\b	T <sub>C</sub> = 25 °C	,	120		
Continuous Drain Current (T <sub>J</sub> = 175 °C) <sup>b</sup>	T <sub>C</sub> = 100 °C	l I <sub>D</sub>	90		
Pulsed Drain Current	I <sub>DM</sub>	350	Α		
Continuous Source Current (Diode Conduction)	I <sub>S</sub>	70 <sup>a</sup>			
Avalanche Current	I <sub>AS</sub>	50			
Single Avalanche Energy (Duty Cycle ≤ 1 %)	L = 0.1 mH	E <sub>AS</sub>	125	mJ	
Maximum Daylar Dissination	T <sub>C</sub> = 25 °C	В	136	- W	
Maximum Power Dissipation	T <sub>A</sub> = 25 °C	P <sub>D</sub>	3 <sup>b</sup> , 8.3 <sup>b, c</sup>		
Operating Junction and Storage Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	- 55 to 175	°C		

THERMAL RESISTANCE RATINGS							
Parameter		Symbol	Typical	Maximum	Unit		
Marian and Luncking to Ambient	t ≤ 10 sec	D	15	18			
Maximum Junction-to-Ambient <sup>a</sup>	Steady State	$R_{thJA}$	40	50	°C/W		
Maximum Junction-to-Case		R <sub>thJC</sub>	0.85	1.1			

### Notes:

- a. Package limited.
- b. Surface mounted on 1" x 1" FR4 board.
- c.  $t \le 10 \text{ s}$ .



<b>SPECIFICATIONS</b> (T <sub>J</sub> = 25 °C, unless otherwise noted)							
Parameter	Symbol	Test Conditions	Min.	Typ.a	Max.	Unit	
Static	•		•				
Drain-Source Breakdown Voltage	V <sub>DS</sub>	$V_{GS} = 0 \text{ V}, I_{D} = 250 \mu\text{A}$	60			V	
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	2		4	V	
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA	
		V <sub>DS</sub> = 60 V, V <sub>GS</sub> = 0 V			1		
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = 60 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 125 °C			50	50 μA 250	
		V <sub>DS</sub> = 60 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 175 °C			250		
On-State Drain Current <sup>b</sup>	I <sub>D(on)</sub>	V <sub>DS</sub> = 5 V, V <sub>GS</sub> = 10 V	60			Α	
		V <sub>GS</sub> = 10 V, I <sub>D</sub> = 20 A	0.005			Ω	
	В	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 20 A, T <sub>J</sub> = 125 °C		0.010 0.015			
Drain-Source On-State Resistance <sup>b</sup>	R <sub>DS(on)</sub>	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 20 A, T <sub>J</sub> = 175 °C					
		V <sub>GS</sub> = 7.5 V, I <sub>D</sub> = 15 A	0.008				
Forward Transconductance <sup>b</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = 15 V, I <sub>D</sub> = 20 A		60		S	
Dynamic							
Input Capacitance	C <sub>iss</sub>			6650			
Output Capacitance	C <sub>oss</sub>	$V_{GS} = 0 \text{ V}, V_{DS} = 25 \text{ V}, f = 1 \text{ MHz}$		570		pF	
Reverse Transfer Capacitance	C <sub>rss</sub>			325			
Total Gate Charge <sup>c</sup>	$Q_g$			47	70	nC	
Gate-Source Charge <sup>c</sup>	$Q_{gs}$	$V_{DS} = 30 \text{ V}, V_{GS} = 10 \text{ V}, I_{D} = 50 \text{ A}$		10			
Gate-Drain Charge <sup>c</sup>	$Q_{gd}$			12			
Turn-On Delay Time <sup>c</sup>	t <sub>d(on)</sub>			10	20		
Rise Time <sup>c</sup>	t <sub>r</sub>	$V_{DD} = 30 \text{ V, R}_{L} = 0.6 \Omega$		15	25	ns	
Turn-Off Delay Time <sup>c</sup>	t <sub>d(off)</sub>	$I_D \cong 50 \text{ A}, V_{GEN} = 10 \text{ V}, R_g = 2.5 \Omega$		35	50		
Fall Time <sup>c</sup>	t <sub>f</sub>			20	30		
Source-Drain Diode Ratings and Cha	aracteristics (	T <sub>C</sub> = 25 °C)					
Pulsed Current	I <sub>SM</sub>			350		Α	
Diode Forward Voltage	$V_{SD}$	$I_F = 20 \text{ A}, V_{GS} = 0 \text{ V}$		1	1.5	V	
Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = 20 A, di/dt = 100 A/μs		45	100	ns	

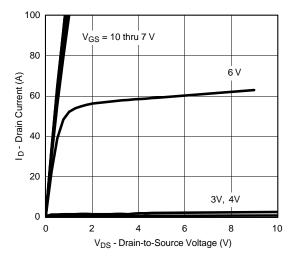
### Notes:

- a. For design aid only; not subject to production testing.
- b. Pulse test; pulse width  $\leq$  300  $\mu$ s, duty cycle  $\leq$  2 %.
- c. Independent of operating temperature.

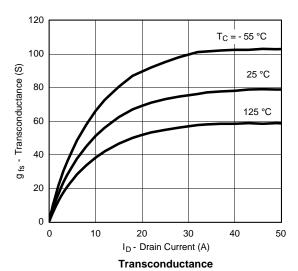
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



# TYPICAL CHARACTERISTICS (25 °C unless noted)



# **Output Characteristics**



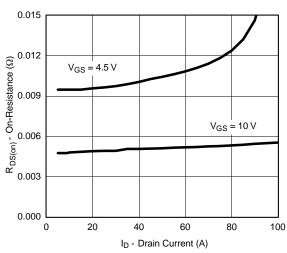
# 8000 7000 6000 Coss 1000 Coss 0 1000 0 20 30 40 50 60

V<sub>DS</sub> - Drain-to-Source Voltage (V)

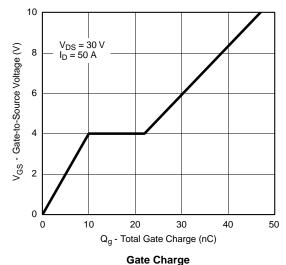
Capacitance

100 80 I<sub>D</sub> - Drain Current (A) 60 40 T<sub>C</sub> = 125 °C 20 25 °C -55 °C 0 0 3.0 4.5 6.0 7.5 V<sub>GS</sub> - Gate-to-Source Voltage (V)

**Transfer Characteristics** 



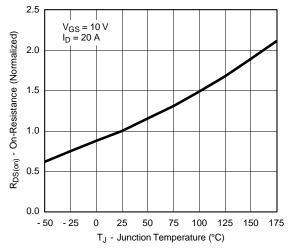
On-Resistance vs. Drain Current



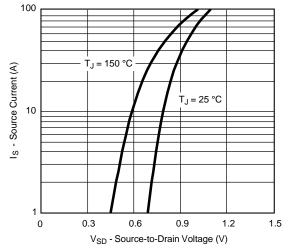
Cate Charge



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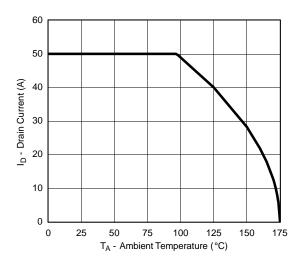
On-Resistance vs. Junction Temperature

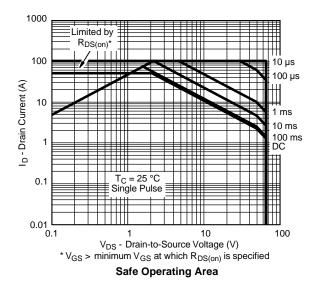


Source-Drain Diode Forward Voltage

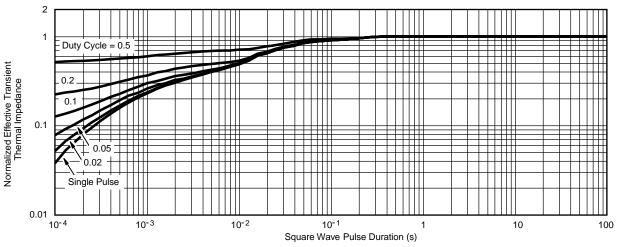


### THERMAL RATINGS





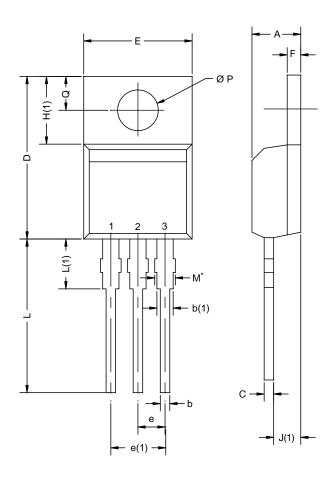
**Maximum Drain Current vs. Ambient Temperature** 



Normalized Thermal Transient Impedance, Junction-to-Case



# **TO-220AB**



	MILLIM	IETERS	INCHES		
DIM.	MIN.	MAX.	MIN.	MAX.	
Α	4.25	4.65	0.167	0.183	
b	0.69	1.01	0.027	0.040	
b(1)	1.20	1.73	0.047	0.068	
С	0.36	0.61	0.014	0.024	
D	14.85	15.49	0.585	0.610	
E	10.04	10.51	0.395	0.414	
е	2.41	2.67	0.095	0.105	
e(1)	4.88	5.28	0.192	0.208	
F	1.14	1.40	0.045	0.055	
H(1)	6.09	6.48	0.240	0.255	
J(1)	2.41	2.92	0.095	0.115	
L	13.35	14.02	0.526	0.552	
L(1)	3.32	3.82	0.131	0.150	
ØΡ	3.54	3.94	0.139	0.155	
Q	2.60	3.00	0.102	0.118	
ECN: X12-0208-Rev. N, 08-Oct-12 DWG: 5471					

### Notes

 $<sup>^{*}</sup>$  M = 1.32 mm to 1.62 mm (dimension including protrusion) Heatsink hole for HVM



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