

N-Channel 30-V (D-S) MOSFET

PRODU	CT SUMMARY		
V _{DS} (V)	$R_{DS(on)}(\Omega)$	I _D (A) ^{a, e}	Q _g (Typ)
30	0.0021 at $V_{GS} = 10 \text{ V}$	140	92 nC
30	0.0029 at $V_{GS} = 4.5 \text{ V}$	130	92 110

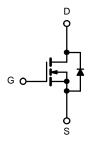
FEATURES

- **DT-Trench Power MOSFET**
- 100 % $\rm R_{\rm g}$ and UIS Tested Compliant to RoHS Directive 2011/65/EU



APPLICATIONS

- OR-ing
- Server
- DC/DC



N-Channel MOSFET

IO-220AE	3
0	
G D S	
Top View	

TO 220 A D

ABSOLUTE MAXIMUM RATINGS ($T_A = 25 ^{\circ}\text{C}$, unle	ess otherwise r	noted)	
Parameter		Symbol	Limit	Unit
Drain-Source Voltage Gate-Source Voltage		V _{DS}	30	V
		V_{GS}	± 20	ľ
	T _C = 25 °C		140 ^{a, e}	
Continuous Drain Current (T,I = 175 °C)	T _C = 70 °C] ,	110 ^e	
Continuous Diain Current (1) = 175 C)	T _A = 25 °C	- I _D -	39 ^{b, c}	A
	T _A = 70 °C		28 ^{b, c}] ^
Pulsed Drain Current		I _{DM}	370	
Avalanche Current Pulse	L = 0.1 mH	I _{AS}	39	
Single Pulse Avalanche Energy	L=0.1 IIII	E _{AS}	375	mJ
Continuous Source-Drain Diode Current	T _C = 25 °C	- I _S	90 ^{a, e}	Α
Continuous Source-Diam Diode Current	T _A = 25 °C	'S	3.13 ^{b, c}	
	T _C = 25 °C		250 ^a	
Maximum Power Dissipation	T _C = 70 °C	P _D	175	W
	T _A = 25 °C	' D	3.75 ^{b, c}	VV
	T _A = 70 °C]	2.63 ^{b, c}]
Operating Junction and Storage Temperature Rang	е	T _J , T _{stg}	- 55 to 175	°C

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Тур.	Max.	Unit
Maximum Junction-to-Ambient ^{b, d}	t ≤ 10 sec	R _{thJA}	32	40	°C/W
Maximum Junction-to-Case	Steady State	R _{thJC}	0.5	0.6	C/ VV

Notes:

- a. Based on T_C = 25 °C.
 b. Surface mounted on 1" x 1" FR4 board.

- b. Striate informed on 1 X 1 114 board.
 c. t = 10 sec.
 d. Maximum under steady state conditions is 90 °C/W.
 e. Calculated based on maximum junction temperature. Package limitation current is 90 A.



Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V _{DS}	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	30			V
V _{DS} Temperature Coefficient	$\Delta V_{DS}/T_{J}$	I _D = 250 μA		35		m\//9C
V _{GS(th)} Temperature Coefficient	$\Delta V_{GS(th)}/T_{J}$	1D = 230 μΑ		- 7.5		mV/°C
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	1.0		3.0	V
Gate-Source Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA
Zoro Coto Voltogo Droin Current	1	V _{DS} = 30 V V _{GS} = 0 V			1	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 24 V V _{GS} = 0 V, T _J = 55 °C			10 µA	
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$	90			Α
	В	$V_{GS} = 10 \text{ V}, I_D = 38.8 \text{ A}$		0.0021		Ω
Drain-Source On-State Resistance ^a	R _{DS(on)}	$V_{GS} = 4.5 \text{ V}, I_D = 37 \text{ A}$		0.0029		
Forward Transconductance ^a	9 _{fs}	$V_{DS} = 15 \text{ V}, I_{D} = 38.8 \text{ A}$		160		S
Dynamic ^b				_		ı
Input Capacitance	C _{iss}			6201		
Output Capacitance	C _{oss}	$V_{DS} = 15 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		1725		pF
Reverse Transfer Capacitance	C _{rss}			970		
Total Cata Charge	Qg	$V_{DS} = 15 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 38.8 \text{ A}$		171	257	nC
Total Gate Charge				81.5	123	
Gate-Source Charge	Q _{gs}	$V_{DS} = 15 \text{ V}, V_{GS} = 4.5 \text{ V}, I_{D} = 28.8 \text{ A}$		34		nc nc
Gate-Drain Charge	Q_{gd}			29		
Gate Resistance	R_g	f = 1 MHz		1.4	2.1	Ω
Turn-On Delay Time	t _{d(on)}			18	27	
Rise Time	t _r	$V_{DD} = 15 \text{ V}, R_{L} = 0.625 \Omega$		11	17	
Turn-Off Delay Time	t _{d(off)}	$I_D\cong 24$ A, V_{GEN} = 10 V, R_g = 1 Ω		70	105	
Fall Time	t _f			10	15	
Turn-On Delay Time	t _{d(on)}			55	83	ns -
Rise Time	t _r	V_{DD} = 15 V, R_L = 0.67 Ω		180	270	
Turn-Off Delay Time	t _{d(off)}	$I_D\cong 22.5$ A, V_{GEN} = 4.5 V, R_g = 1 Ω		55	83	
Fall Time	t _f			12	18	
Drain-Source Body Diode Characteristic	s					
Continuous Source-Drain Diode Current	I _S	$T_C = 25 ^{\circ}C$		140		۸
Pulse Diode Forward Current ^a	I _{SM}			370		Α
Body Diode Voltage	V _{SD}	I _S = 22 A		8.0	1.2	V
Body Diode Reverse Recovery Time	t _{rr}			52	78	ns
Body Diode Reverse Recovery Charge	Q _{rr}	I _F = 20 A, di/dt = 100 A/µs, T _{.I} = 25 °C		70.2	105	nC
Reverse Recovery Fall Time	t _a	1 _F - 20 A, α//αι = 100 Α/μ5, 1 _J = 25 °C		27		20
Reverse Recovery Rise Time	t _b			25		ns

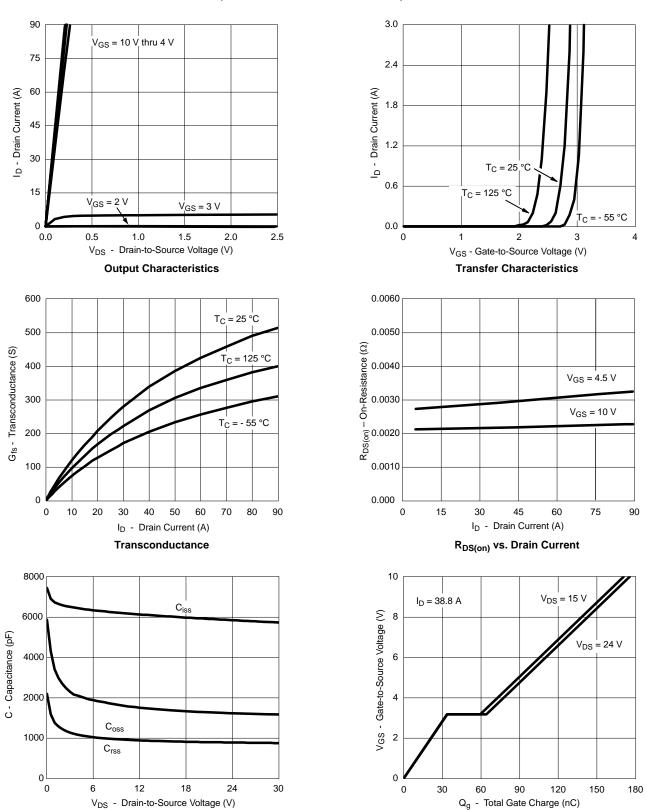
Notes:

- a. Pulse test; pulse width $\leq 300~\mu s,$ duty cycle $\leq 2~\%.$
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

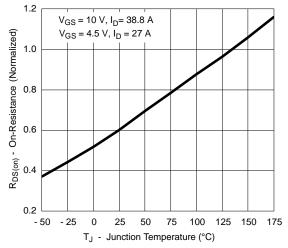


Capacitance

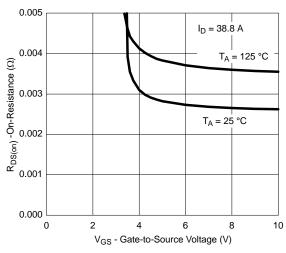
Gate Charge



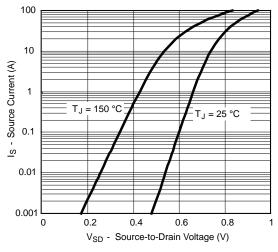
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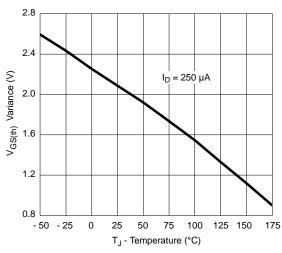
On-Resistance vs. Junction Temperature



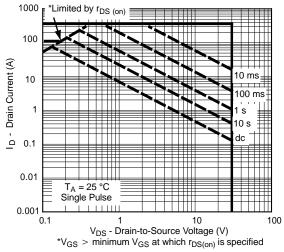
 $R_{DS(on)}$ vs. V_{GS} vs. Temperature



Forward Diode Voltage vs. Temperature



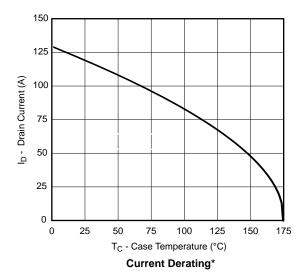
Threshold Voltage

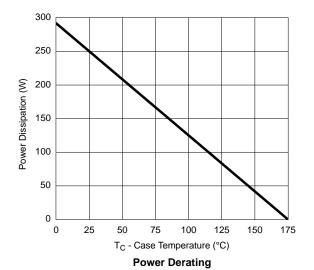


Safe Operating Area, Junction-to-Ambient

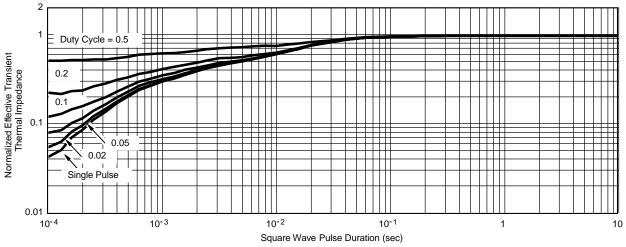


TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)





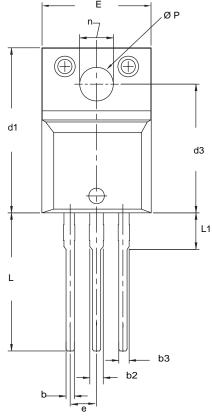
*The power dissipation P_D is based on $T_{J(max)}$ = 175 °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

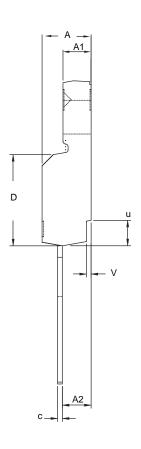


Normalized Thermal Transient Impedance, Junction-to-Case



TO-220 FULLPAK (HIGH VOLTAGE)





DIM.	MILLIN	METERS	INCHES		
	MIN.	MAX.	MIN.	MAX.	
Α	4.570	4.830	0.180	0.190	
A1	2.570	2.830	0.101	0.111	
A2	2.510	2.850	0.099	0.112	
b	0.622	0.890	0.024	0.035	
b2	1.229	1.400	0.048	0.055	
b3	1.229	1.400	0.048	0.055	
С	0.440	0.629	0.017	0.025	
D	8.650	9.800	0.341	0.386	
d1	15.88	16.120	0.622	0.635	
d3	12.300	12.920 0.484	0.484	0.509	
E	10.360	10.630	0.408	0.419	
е	2.54	BSC	0.100 BSC		
L	13.200	13.730	0.520	0.541	
L1	3.100	3.500	0.122	0.138	
n	6.050	6.150	0.238	0.242	
ØΡ	3.050	3.450	0.120	0.136	
u	2.400	2.500	0.094	0.098	
٧	0.400	0.500	0.016	0.020	

ECN: X09-0126-Rev. B, 26-Oct-09 DWG: 5972

- 1. To be used only for process drawing. 2. These dimensions apply to all TO-220, FULLPAK leadframe versions 3 leads. 3. All critical dimensions should C meet $C_{pk} > 1.33$.
- 4. All dimensions include burrs and plating thickness.
 5. No chipping or package damage.



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