

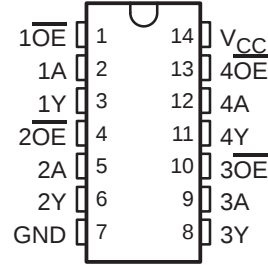
# SN74LVT125

## 3.3-V ABT QUADRUPLE BUS BUFFER WITH 3-STATE OUTPUTS

SCBS133F – MAY 1992 – REVISED OCTOBER 2003

- Supports Mixed-Mode Signal Operation (5-V Input and Output Voltages With 3.3-V  $V_{CC}$ )
- Supports Unregulated Battery Operation Down to 2.7 V
- Typical  $V_{OLP}$  (Output Ground Bounce)  $<0.8$  V at  $V_{CC} = 3.3$  V,  $T_A = 25^\circ\text{C}$
- $I_{off}$  Supports Partial-Power-Down Mode Operation
- Bus-Hold Data Inputs Eliminate the Need for External Pullup Resistors
- Latch-Up Performance Exceeds 500 mA Per JEDEC Standard JESD-17
- ESD Protection Exceeds JESD 22
  - 2000-V Human-Body Model (A114-A)
  - 200-V Machine Model (A115-A)

D, DB, NS, OR PW PACKAGE  
(TOP VIEW)



### description/ordering information

This bus buffer is designed specifically for low-voltage (3.3-V)  $V_{CC}$  operation, but with the capability to provide a TTL interface to a 5-V system environment.

The SN74LVT125 features independent line drivers with 3-state outputs. Each output is in the high-impedance state when the associated output-enable ( $\overline{OE}$ ) input is high.

Active bus-hold circuitry holds unused or undriven inputs at a valid logic state. Use of pullup or pulldown resistors with the bus-hold circuitry is not recommended.

This device is fully specified for partial-power-down applications using  $I_{off}$ . The  $I_{off}$  circuitry disables the outputs, preventing damaging current backflow through the device when it is powered down.

To ensure the high-impedance state during power up or power down,  $\overline{OE}$  should be tied to  $V_{CC}$  through a pullup resistor; the minimum value of the resistor is determined by the current-sinking capability of the driver.

### ORDERING INFORMATION

| $T_A$         | PACKAGE <sup>†</sup> |               | ORDERABLE PART NUMBER | TOP-SIDE MARKING |
|---------------|----------------------|---------------|-----------------------|------------------|
| –40°C to 85°C | SOIC – D             | Tube          | SN74LVT125D           | LVT125           |
|               |                      | Tape and reel | SN74LVT125DR          |                  |
|               | SOP – NS             | Tape and reel | SN74LVT125NSR         | LVT125           |
|               | SSOP – DB            | Tape and reel | SN74LVT125DBR         | LX125            |
|               | TSSOP – PW           | Tube          | SN74LVT125PW          | LX125            |
|               |                      | Tape and reel | SN74LVT125PWR         |                  |

<sup>†</sup> Package drawings, standard packing quantities, thermal data, symbolization, and PCB design guidelines are available at [www.ti.com/sc/package](http://www.ti.com/sc/package).



Please be aware that an important notice concerning availability, standard warranty, and use in critical applications of Texas Instruments semiconductor products and disclaimers thereto appears at the end of this data sheet.

PRODUCTION DATA information is current as of publication date. Products conform to specifications per the terms of Texas Instruments standard warranty. Production processing does not necessarily include testing of all parameters.

**TEXAS  
INSTRUMENTS**

POST OFFICE BOX 655303 • DALLAS, TEXAS 75265

Copyright © 2003, Texas Instruments Incorporated

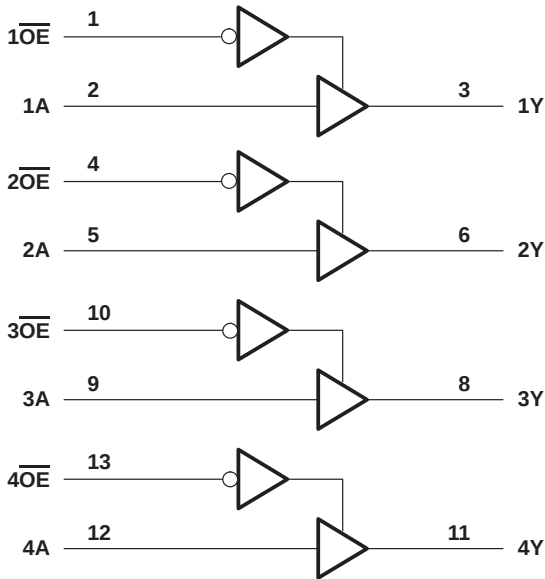
SN74LVT125  
3.3-V ABT QUADRUPLE BUS BUFFER  
WITH 3-STATE OUTPUTS

SCBS133F – MAY 1992 – REVISED OCTOBER 2003

FUNCTION TABLE  
(each buffer)

| INPUTS                 |   | OUTPUT |
|------------------------|---|--------|
| $\overline{\text{OE}}$ | A | Y      |
| L                      | H | H      |
| L                      | L | L      |
| H                      | X | Z      |

logic diagram (positive logic)



absolute maximum ratings over operating free-air temperature range (unless otherwise noted)<sup>†</sup>

|                                                                                              |                 |
|----------------------------------------------------------------------------------------------|-----------------|
| Supply voltage range, $V_{CC}$                                                               | –0.5 V to 4.6 V |
| Input voltage range, $V_I$ (see Note 1)                                                      | –0.5 V to 7 V   |
| Voltage range applied to any output in the high state or power-off state, $V_O$ (see Note 1) | –0.5 V to 7 V   |
| Current into any output in the low state, $I_O$                                              | 128 mA          |
| Current into any output in the high state, $I_O$ (see Note 2)                                | 64 mA           |
| Input clamp current, $I_{IK}$ ( $V_I < 0$ )                                                  | –50 mA          |
| Output clamp current, $I_{OK}$ ( $V_O < 0$ )                                                 | –50 mA          |
| Package thermal impedance, $\theta_{JA}$ (see Note 3):                                       |                 |
| D package                                                                                    | 86°C/W          |
| DB package                                                                                   | 96°C/W          |
| NS package                                                                                   | 76°C/W          |
| PW package                                                                                   | 113°C/W         |
| Storage temperature range, $T_{stg}$                                                         | –65°C to 150°C  |

<sup>†</sup> Stresses beyond those listed under “absolute maximum ratings” may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under “recommended operating conditions” is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

NOTES: 1. The input and output negative-voltage ratings may be exceeded if the input and output clamp-current ratings are observed.  
2. This current flows only when the output is in the high state and  $V_O > V_{CC}$ .  
3. The package thermal impedance is calculated in accordance with JESD 51-7.

# SN74LVT125

## 3.3-V ABT QUADRUPLE BUS BUFFER WITH 3-STATE OUTPUTS

SCBS133F – MAY 1992 – REVISED OCTOBER 2003

### recommended operating conditions (see Note 4)

|                     |                                    | MIN             | MAX | UNIT    |
|---------------------|------------------------------------|-----------------|-----|---------|
| $V_{CC}$            | Supply voltage                     | 2.7             | 3.6 | V       |
| $V_{IH}$            | High-level input voltage           | 2               |     | V       |
| $V_{IL}$            | Low-level input voltage            |                 | 0.8 | V       |
| $V_I$               | Input voltage                      |                 | 5.5 | V       |
| $I_{OH}$            | High-level output current          |                 | -32 | mA      |
| $I_{OL}$            | Low-level output current           |                 | 64  | mA      |
| $\Delta t/\Delta v$ | Input transition rise or fall rate | Outputs enabled |     | 10 ns/V |
| $T_A$               | Operating free-air temperature     | -40             | 85  | °C      |

NOTE 4: All unused inputs of the device must be held at  $V_{CC}$  or GND to ensure proper device operation. Refer to the TI application report, *Implications of Slow or Floating CMOS Inputs*, literature number SCBA004.

### electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

| PARAMETER            | TEST CONDITIONS                                           |                                                                                  |                  | MIN          | TYP <sup>†</sup> | MAX       | UNIT          |
|----------------------|-----------------------------------------------------------|----------------------------------------------------------------------------------|------------------|--------------|------------------|-----------|---------------|
| $V_{IK}$             | $V_{CC} = 2.7\text{ V}$ ,                                 | $I_I = -18\text{ mA}$                                                            |                  |              |                  | -1.2      | V             |
| $V_{OH}$             | $V_{CC} = \text{MIN to MAX}^\ddagger$ ,                   | $I_{OH} = -100\text{ }\mu\text{A}$                                               |                  | $V_{CC}-0.2$ |                  |           | V             |
|                      | $V_{CC} = 2.7\text{ V}$ ,                                 | $I_{OH} = -8\text{ mA}$                                                          |                  | 2.4          |                  |           |               |
|                      | $V_{CC} = 3\text{ V}$                                     | $I_{OH} = -32\text{ mA}$                                                         |                  | 2            |                  |           |               |
| $V_{OL}$             | $V_{CC} = 2.7\text{ V}$                                   | $I_{OL} = 100\text{ }\mu\text{A}$                                                |                  |              |                  | 0.2       | V             |
|                      |                                                           | $I_{OL} = 24\text{ mA}$                                                          |                  |              |                  | 0.5       |               |
|                      | $V_{CC} = 3\text{ V}$                                     | $I_{OL} = 16\text{ mA}$                                                          |                  |              |                  | 0.4       |               |
|                      |                                                           | $I_{OL} = 32\text{ mA}$                                                          |                  |              |                  | 0.5       |               |
|                      |                                                           | $I_{OL} = 64\text{ mA}$                                                          |                  |              |                  | 0.55      |               |
| $I_I$                | $V_{CC} = 0\text{ or MAX}^\ddagger$ ,                     | $V_I = 5.5\text{ V}$                                                             |                  |              |                  | 10        | $\mu\text{A}$ |
|                      | $V_{CC} = 3.6\text{ V}$                                   | $V_I = V_{CC}\text{ or GND}$                                                     | Control inputs   |              |                  | $\pm 1$   |               |
|                      |                                                           | $V_I = V_{CC}$                                                                   | Data inputs      |              |                  | 1         |               |
|                      |                                                           | $V_I = 0$                                                                        |                  |              |                  | -5        |               |
| $I_{off}$            | $V_{CC} = 0$ ,                                            | $V_I\text{ or }V_O = 0\text{ to }4.5\text{ V}$                                   |                  |              |                  | $\pm 100$ | $\mu\text{A}$ |
| $I_{I(\text{hold})}$ | $V_{CC} = 3\text{ V}$                                     | $V_I = 0.8\text{ V}$                                                             | Data inputs      | 75           |                  |           | $\mu\text{A}$ |
|                      |                                                           | $V_I = 2\text{ V}$                                                               |                  | -75          |                  |           | $\mu\text{A}$ |
| $I_{OZH}$            | $V_{CC} = 3.6\text{ V}$ ,                                 | $V_O = 3\text{ V}$                                                               |                  |              |                  | 5         | $\mu\text{A}$ |
| $I_{OZL}$            | $V_{CC} = 3.6\text{ V}$ ,                                 | $V_O = 0.5\text{ V}$                                                             |                  |              |                  | -5        | $\mu\text{A}$ |
| $I_{CC}$             | $V_{CC} = 3.6\text{ V}$ ,<br>$V_I = V_{CC}\text{ or GND}$ | $I_O = 0$ ,                                                                      | Outputs high     | 0.12         | 0.19             |           | mA            |
|                      |                                                           |                                                                                  | Outputs low      | 4.5          | 7                |           |               |
|                      |                                                           |                                                                                  | Outputs disabled | 0.12         | 0.19             |           |               |
| $\Delta I_{CC}^\S$   | $V_{CC} = 3\text{ V to }3.6\text{ V}$ ,                   | One input at $V_{CC} - 0.6\text{ V}$ ,<br>Other inputs at $V_{CC}\text{ or GND}$ |                  |              |                  | 0.2       | mA            |
| $C_i$                | $V_I = 3\text{ V or }0$                                   |                                                                                  |                  |              |                  | 4         | pF            |
| $C_O$                | $V_O = 3\text{ V or }0$                                   |                                                                                  |                  |              |                  | 8         | pF            |

<sup>†</sup> All typical values are at  $V_{CC} = 3.3\text{ V}$ ,  $T_A = 25^\circ\text{C}$ .

<sup>‡</sup> For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions.

<sup>§</sup> This is the increase in supply current for each input that is at the specified TTL voltage level, rather than  $V_{CC}$  or GND.



# SN74LVT125

## 3.3-V ABT QUADRUPLE BUS BUFFER

### WITH 3-STATE OUTPUTS

SCBS133F – MAY 1992 – REVISED OCTOBER 2003

switching characteristics over recommended operating free-air temperature range,  $C_L = 50$  pF (unless otherwise noted) (see Figure 1)

| PARAMETER | FROM<br>(INPUT) | TO<br>(OUTPUT) | $V_{CC} = 3.3\text{ V}$<br>$\pm 0.3\text{ V}$ |      |     | $V_{CC} = 2.7\text{ V}$ |     | UNIT |
|-----------|-----------------|----------------|-----------------------------------------------|------|-----|-------------------------|-----|------|
|           |                 |                | MIN                                           | TYP† | MAX | MIN                     | MAX |      |
| $t_{PLH}$ | A               | Y              | 1                                             | 2.7  | 4   | 4.5                     |     | ns   |
| $t_{PHL}$ |                 |                | 1                                             | 2.9  | 3.9 | 4.9                     |     |      |
| $t_{PZH}$ | $\overline{OE}$ | Y              | 1                                             | 3.4  | 4.7 | 6                       |     | ns   |
| $t_{PZL}$ |                 |                | 1.1                                           | 3.4  | 4.7 | 6.5                     |     |      |
| $t_{PHZ}$ | $\overline{OE}$ | Y              | 1.8                                           | 3.7  | 5.1 | 5.7                     |     | ns   |
| $t_{PLZ}$ |                 |                | 1.3                                           | 2.6  | 4.5 | 4                       |     |      |

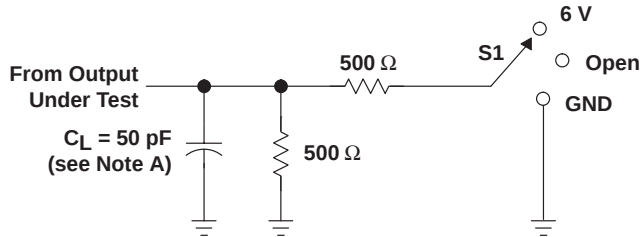
† All typical values are at  $V_{CC} = 3.3\text{ V}$ ,  $T_A = 25^\circ\text{C}$ .

# SN74LVT125

## 3.3-V ABT QUADRUPLE BUS BUFFER WITH 3-STATE OUTPUTS

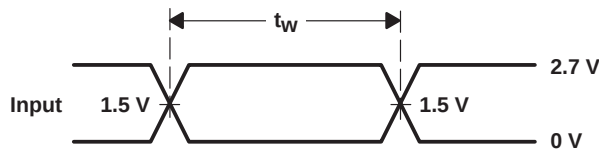
SCBS133F – MAY 1992 – REVISED OCTOBER 2003

### PARAMETER MEASUREMENT INFORMATION

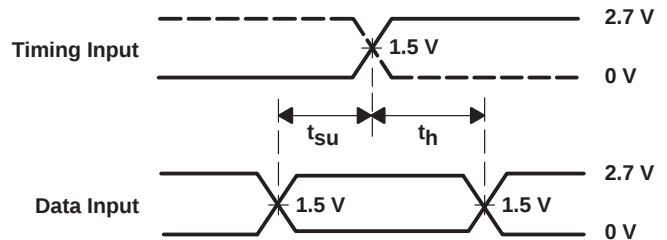


LOAD CIRCUIT FOR OUTPUTS

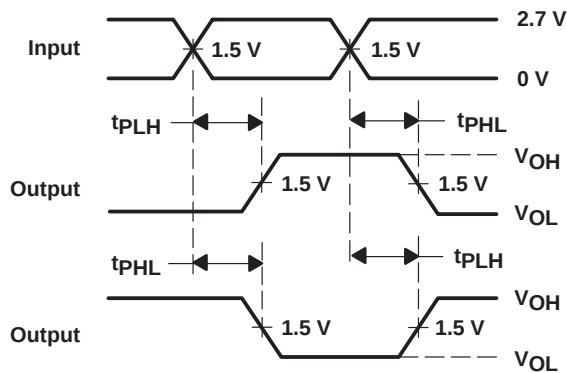
| TEST              | S1   |
|-------------------|------|
| $t_{PLH}/t_{PHL}$ | Open |
| $t_{PLZ}/t_{PZL}$ | 6 V  |
| $t_{PHZ}/t_{PZH}$ | GND  |



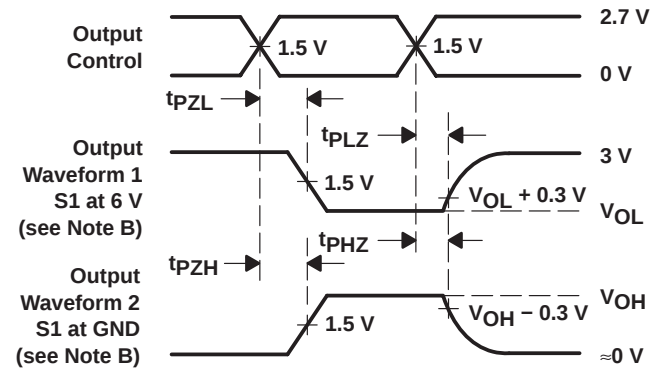
VOLTAGE WAVEFORMS  
PULSE DURATION



VOLTAGE WAVEFORMS  
SETUP AND HOLD TIMES



VOLTAGE WAVEFORMS  
PROPAGATION DELAY TIMES  
INVERTING AND NONINVERTING OUTPUTS



VOLTAGE WAVEFORMS  
ENABLE AND DISABLE TIMES  
LOW- AND HIGH-LEVEL ENABLING

- NOTES: A.  $C_L$  includes probe and jig capacitance.
- B. Waveform 1 is for an output with internal conditions such that the output is low except when disabled by the output control. Waveform 2 is for an output with internal conditions such that the output is high except when disabled by the output control.
- C. All input pulses are supplied by generators having the following characteristics:  $PRR \leq 10$  MHz,  $Z_O = 50 \Omega$ ,  $t_r \leq 2.5$  ns,  $t_f \leq 2.5$  ns.
- D. The outputs are measured one at a time with one transition per measurement.
- E. All parameters and waveforms are not applicable to all devices.

Figure 1. Load Circuit and Voltage Waveforms

**PACKAGING INFORMATION**

| Orderable Device | Status <sup>(1)</sup> | Package Type | Package Drawing | Pins | Package Qty | Eco Plan <sup>(2)</sup> | Lead/Ball Finish | MSL Peak Temp <sup>(3)</sup> |
|------------------|-----------------------|--------------|-----------------|------|-------------|-------------------------|------------------|------------------------------|
| SN74LVT125D      | NRND                  | SOIC         | D               | 14   | 50          | Green (RoHS & no Sb/Br) | CU NIPDAU        | Level-1-260C-UNLIM           |
| SN74LVT125DBLE   | OBSOLETE              | SSOP         | DB              | 14   |             | TBD                     | Call TI          | Call TI                      |
| SN74LVT125DBR    | NRND                  | SSOP         | DB              | 14   | 2000        | Green (RoHS & no Sb/Br) | CU NIPDAU        | Level-1-260C-UNLIM           |
| SN74LVT125DBRG4  | NRND                  | SSOP         | DB              | 14   | 2000        | Green (RoHS & no Sb/Br) | CU NIPDAU        | Level-1-260C-UNLIM           |
| SN74LVT125DG4    | NRND                  | SOIC         | D               | 14   | 50          | Green (RoHS & no Sb/Br) | CU NIPDAU        | Level-1-260C-UNLIM           |
| SN74LVT125DR     | NRND                  | SOIC         | D               | 14   | 2500        | Green (RoHS & no Sb/Br) | CU NIPDAU        | Level-1-260C-UNLIM           |
| SN74LVT125DRG4   | NRND                  | SOIC         | D               | 14   | 2500        | Green (RoHS & no Sb/Br) | CU NIPDAU        | Level-1-260C-UNLIM           |
| SN74LVT125NSR    | NRND                  | SO           | NS              | 14   | 2000        | Green (RoHS & no Sb/Br) | CU NIPDAU        | Level-1-260C-UNLIM           |
| SN74LVT125PW     | NRND                  | TSSOP        | PW              | 14   | 90          | Green (RoHS & no Sb/Br) | CU NIPDAU        | Level-1-260C-UNLIM           |
| SN74LVT125PWG4   | NRND                  | TSSOP        | PW              | 14   | 90          | Green (RoHS & no Sb/Br) | CU NIPDAU        | Level-1-260C-UNLIM           |
| SN74LVT125PWLE   | OBSOLETE              | TSSOP        | PW              | 14   |             | TBD                     | Call TI          | Call TI                      |
| SN74LVT125PWR    | NRND                  | TSSOP        | PW              | 14   | 2000        | Green (RoHS & no Sb/Br) | CU NIPDAU        | Level-1-260C-UNLIM           |
| SN74LVT125PWRG4  | NRND                  | TSSOP        | PW              | 14   | 2000        | Green (RoHS & no Sb/Br) | CU NIPDAU        | Level-1-260C-UNLIM           |

<sup>(1)</sup> The marketing status values are defined as follows:

**ACTIVE:** Product device recommended for new designs.

**LIFEBUY:** TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

**NRND:** Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

**PREVIEW:** Device has been announced but is not in production. Samples may or may not be available.

**OBSOLETE:** TI has discontinued the production of the device.

<sup>(2)</sup> Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS), Pb-Free (RoHS Exempt), or Green (RoHS & no Sb/Br) - please check <http://www.ti.com/productcontent> for the latest availability information and additional product content details.

**TBD:** The Pb-Free/Green conversion plan has not been defined.

**Pb-Free (RoHS):** TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes.

**Pb-Free (RoHS Exempt):** This component has a RoHS exemption for either 1) lead-based flip-chip solder bumps used between the die and package, or 2) lead-based die adhesive used between the die and leadframe. The component is otherwise considered Pb-Free (RoHS compatible) as defined above.

**Green (RoHS & no Sb/Br):** TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

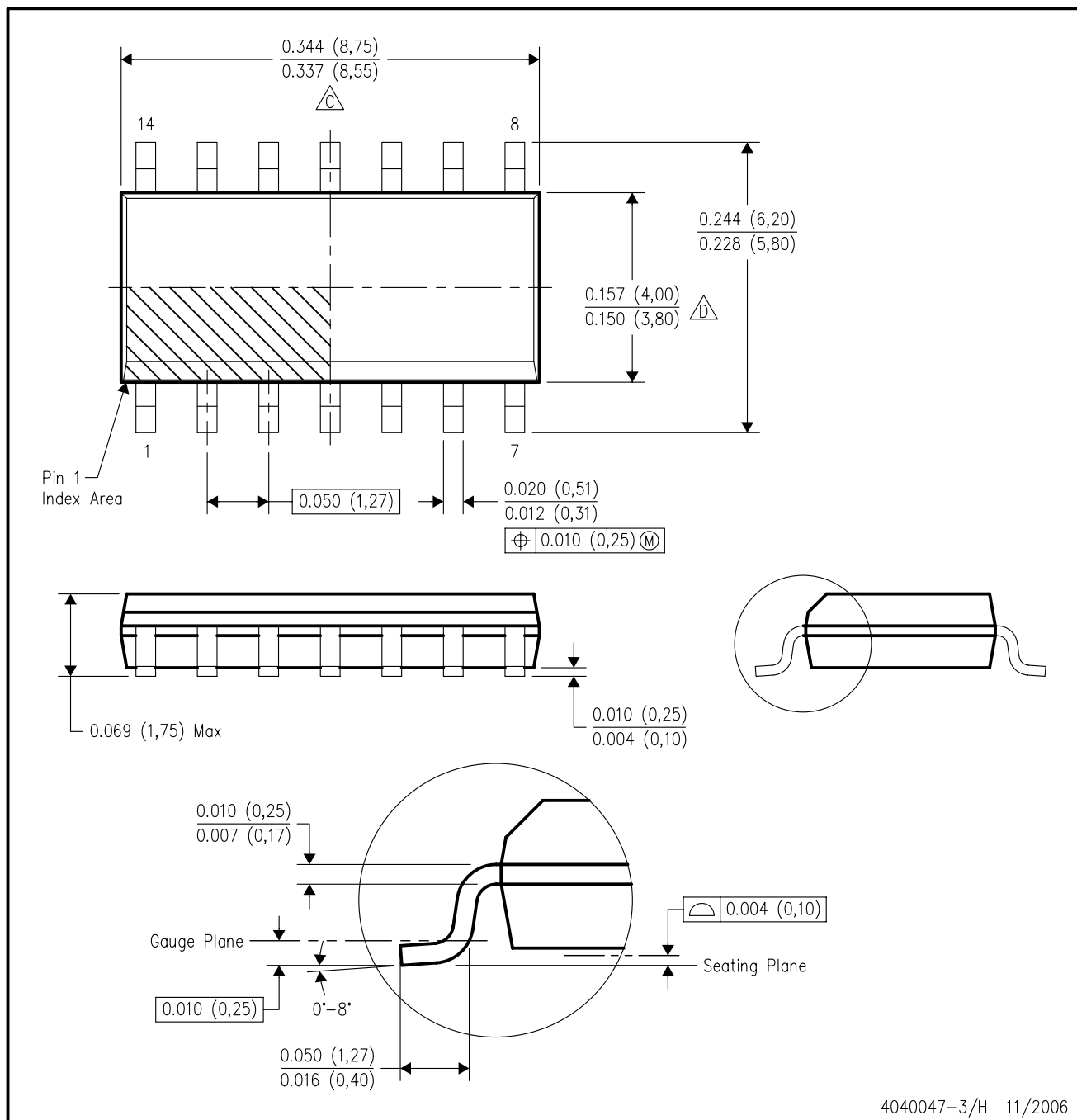
<sup>(3)</sup> MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

**Important Information and Disclaimer:** The information provided on this page represents TI's knowledge and belief as of the date that it is provided. TI bases its knowledge and belief on information provided by third parties, and makes no representation or warranty as to the accuracy of such information. Efforts are underway to better integrate information from third parties. TI has taken and continues to take reasonable steps to provide representative and accurate information but may not have conducted destructive testing or chemical analysis on incoming materials and chemicals. TI and TI suppliers consider certain information to be proprietary, and thus CAS numbers and other limited information may not be available for release.

In no event shall TI's liability arising out of such information exceed the total purchase price of the TI part(s) at issue in this document sold by TI to Customer on an annual basis.

## D (R-PDSO-G14)

## PLASTIC SMALL-OUTLINE PACKAGE



4040047-3/H 11/2006

## NOTES:

- A. All linear dimensions are in inches (millimeters).
- B. This drawing is subject to change without notice.
- C. Body length does not include mold flash, protrusions, or gate burrs. Mold flash, protrusions, or gate burrs shall not exceed .006 (0,15) per end.
- D. Body width does not include interlead flash. Interlead flash shall not exceed .017 (0,43) per side.
- E. Reference JEDEC MS-012 variation AB.

# MECHANICAL DATA

NS (R-PDSO-G\*\*)

PLASTIC SMALL-OUTLINE PACKAGE

14-PINS SHOWN



- NOTES:
- All linear dimensions are in millimeters.
  - This drawing is subject to change without notice.
  - Body dimensions do not include mold flash or protrusion, not to exceed 0,15.

## DB (R-PDSO-G\*\*)

## PLASTIC SMALL-OUTLINE

28 PINS SHOWN



- NOTES: A. All linear dimensions are in millimeters.  
 B. This drawing is subject to change without notice.  
 C. Body dimensions do not include mold flash or protrusion not to exceed 0,15.  
 D. Falls within JEDEC MO-150

## PW (R-PDSO-G\*\*)

## PLASTIC SMALL-OUTLINE PACKAGE

14 PINS SHOWN



- NOTES: A. All linear dimensions are in millimeters.  
 B. This drawing is subject to change without notice.  
 C. Body dimensions do not include mold flash or protrusion not to exceed 0,15.  
 D. Falls within JEDEC MO-153

## IMPORTANT NOTICE

Texas Instruments Incorporated and its subsidiaries (TI) reserve the right to make corrections, modifications, enhancements, improvements, and other changes to its products and services at any time and to discontinue any product or service without notice. Customers should obtain the latest relevant information before placing orders and should verify that such information is current and complete. All products are sold subject to TI's terms and conditions of sale supplied at the time of order acknowledgment.

TI warrants performance of its hardware products to the specifications applicable at the time of sale in accordance with TI's standard warranty. Testing and other quality control techniques are used to the extent TI deems necessary to support this warranty. Except where mandated by government requirements, testing of all parameters of each product is not necessarily performed.

TI assumes no liability for applications assistance or customer product design. Customers are responsible for their products and applications using TI components. To minimize the risks associated with customer products and applications, customers should provide adequate design and operating safeguards.

TI does not warrant or represent that any license, either express or implied, is granted under any TI patent right, copyright, mask work right, or other TI intellectual property right relating to any combination, machine, or process in which TI products or services are used. Information published by TI regarding third-party products or services does not constitute a license from TI to use such products or services or a warranty or endorsement thereof. Use of such information may require a license from a third party under the patents or other intellectual property of the third party, or a license from TI under the patents or other intellectual property of TI.

Reproduction of information in TI data books or data sheets is permissible only if reproduction is without alteration and is accompanied by all associated warranties, conditions, limitations, and notices. Reproduction of this information with alteration is an unfair and deceptive business practice. TI is not responsible or liable for such altered documentation.

Resale of TI products or services with statements different from or beyond the parameters stated by TI for that product or service voids all express and any implied warranties for the associated TI product or service and is an unfair and deceptive business practice. TI is not responsible or liable for any such statements.

Following are URLs where you can obtain information on other Texas Instruments products and application solutions:

| <b>Products</b>    |                                                                    | <b>Applications</b> |                                                                          |
|--------------------|--------------------------------------------------------------------|---------------------|--------------------------------------------------------------------------|
| Amplifiers         | <a href="http://amplifier.ti.com">amplifier.ti.com</a>             | Audio               | <a href="http://www.ti.com/audio">www.ti.com/audio</a>                   |
| Data Converters    | <a href="http://dataconverter.ti.com">dataconverter.ti.com</a>     | Automotive          | <a href="http://www.ti.com/automotive">www.ti.com/automotive</a>         |
| DSP                | <a href="http://dsp.ti.com">dsp.ti.com</a>                         | Broadband           | <a href="http://www.ti.com/broadband">www.ti.com/broadband</a>           |
| Interface          | <a href="http://interface.ti.com">interface.ti.com</a>             | Digital Control     | <a href="http://www.ti.com/digitalcontrol">www.ti.com/digitalcontrol</a> |
| Logic              | <a href="http://logic.ti.com">logic.ti.com</a>                     | Military            | <a href="http://www.ti.com/military">www.ti.com/military</a>             |
| Power Mgmt         | <a href="http://power.ti.com">power.ti.com</a>                     | Optical Networking  | <a href="http://www.ti.com/opticalnetwork">www.ti.com/opticalnetwork</a> |
| Microcontrollers   | <a href="http://microcontroller.ti.com">microcontroller.ti.com</a> | Security            | <a href="http://www.ti.com/security">www.ti.com/security</a>             |
| Low Power Wireless | <a href="http://www.ti.com/lpw">www.ti.com/lpw</a>                 | Telephony           | <a href="http://www.ti.com/telephony">www.ti.com/telephony</a>           |
|                    |                                                                    | Video & Imaging     | <a href="http://www.ti.com/video">www.ti.com/video</a>                   |
|                    |                                                                    | Wireless            | <a href="http://www.ti.com/wireless">www.ti.com/wireless</a>             |

Mailing Address: Texas Instruments  
Post Office Box 655303 Dallas, Texas 75265

Copyright © 2006, Texas Instruments Incorporated