

20 V, single P-channel Trench MOSFET 20 September 2012

**Product data sheet** 

## 1. Product profile

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#### 1.1 General description

P-channel enhancement mode Field-Effect Transistor (FET) in a leadless medium power DFN2020MD-6 (SOT1220) Surface-Mounted Device (SMD) plastic package using Trench MOSFET technology.

#### **1.2 Features and benefits**

- Trench MOSFET technology
- Small and leadless ultra thin SMD plastic package: 2 x 2 x 0.65 mm
- Exposed drain pad for excellent thermal conduction
- Tin-plated 100 % solderable side pads for optical solder inspection

#### 1.3 Applications

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- Charging switch for portable devices
- DC-to-DC converters
- Power management in battery-driven portable devices
- Hard disk and computing power management

#### 1.4 Quick reference data

Table 1. Qui	ck reference data						
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V <sub>DS</sub>	drain-source voltage	T <sub>j</sub> = 25 °C		-	-	-20	V
V <sub>GS</sub>	gate-source voltage			-12	-	12	V
I <sub>D</sub>	drain current	V <sub>GS</sub> = -4.5 V; T <sub>amb</sub> = 25 °C; t ≤ 5 s	[1]	-	-	-10.3	А
Static charact	eristics	·		·			
R <sub>DSon</sub>	drain-source on-state resistance	$V_{GS}$ = -4.5 V; I <sub>D</sub> = -7.2 A; T <sub>j</sub> = 25 °C		-	19	22.5	mΩ

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated, mounting pad for drain 6 cm<sup>2</sup>.

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## 2. Pinning information

Table 2.	Pinning	information		
Pin	Symbol	Description	Simplified outline	Graphic symbol
1	D	drain		D
2	D	drain		
3	G	gate		G
4	S	source		\$ 017aaa257
5	D	drain	Transparent top view	
6	D	drain	DFN2020MD-6 (SOT1220)	
7	D	drain		
8	S	source		

## 3. Ordering information

Table 3. Ordering information						
Type number	Package	age				
	Name	Description	Version			
PMPB19XP	DFN2020MD-6	plastic thermal enhanced ultra thin small outline package; no leads; 6 terminals	SOT1220			

## 4. Marking

Table 4. Marking codes	
Type number	Marking code
PMPB19XP	1R

## 5. Limiting values

#### Table 5.Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

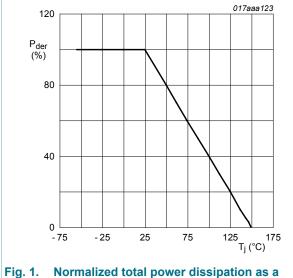
Symbol	Parameter	Conditions		Min	Max	Unit
V <sub>DS</sub>	drain-source voltage	T <sub>j</sub> = 25 °C		-	-20	V
V <sub>GS</sub>	gate-source voltage			-12	12	V
I <sub>D</sub>	drain current	$V_{GS}$ = -4.5 V; $T_{amb}$ = 25 °C; t ≤ 5 s	[1]	-	-10.3	А
		V <sub>GS</sub> = -4.5 V; T <sub>amb</sub> = 25 °C	[1]	-	-7.2	А
		V <sub>GS</sub> = -4.5 V; T <sub>amb</sub> = 100 °C	[1]	-	-4.5	А
I <sub>DM</sub>	peak drain current	$T_{amb}$ = 25 °C; single pulse; $t_p \le 10 \ \mu s$		-	-30	А
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> = 25 °C	[1]	-	1.7	W
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Symbol	Parameter	Conditions		Min	Max	Unit
		T <sub>amb</sub> = 25 °C; t ≤ 5 s	[1]	-	3.5	W
		T <sub>sp</sub> = 25 °C		-	12.5	W
Tj	junction temperature			-55	150	°C
T <sub>amb</sub>	ambient temperature			-55	150	°C
T <sub>stg</sub>	storage temperature			-65	150	°C
Source-dra	in diode	''	I			,
I <sub>S</sub>	source current	T <sub>amb</sub> = 25 °C	[1]	-	-1.9	А

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated, mounting pad for drain 6 cm<sup>2</sup>.



function of junction temperature

$$P_{der} = \frac{P_{tot}}{P_{tot(25^{\circ}C)}} \times 100 \%$$

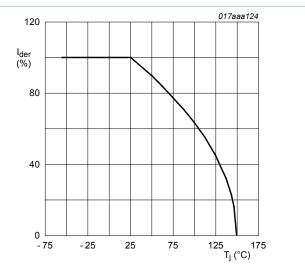
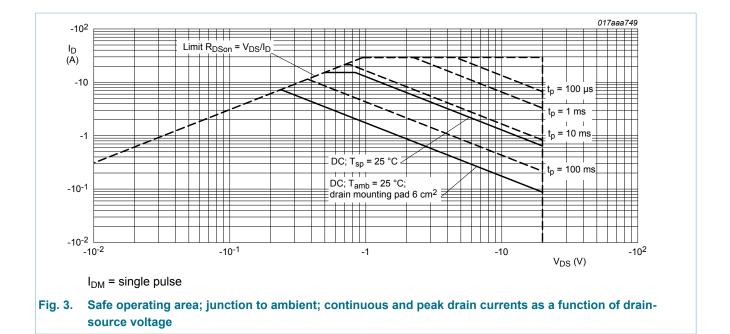


Fig. 2. Normalized continuous drain current as a function of junction temperature

$$I_{der} = \frac{I_D}{I_{D(25^\circ C)}} \times 100 \%$$

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## 6. Thermal characteristics

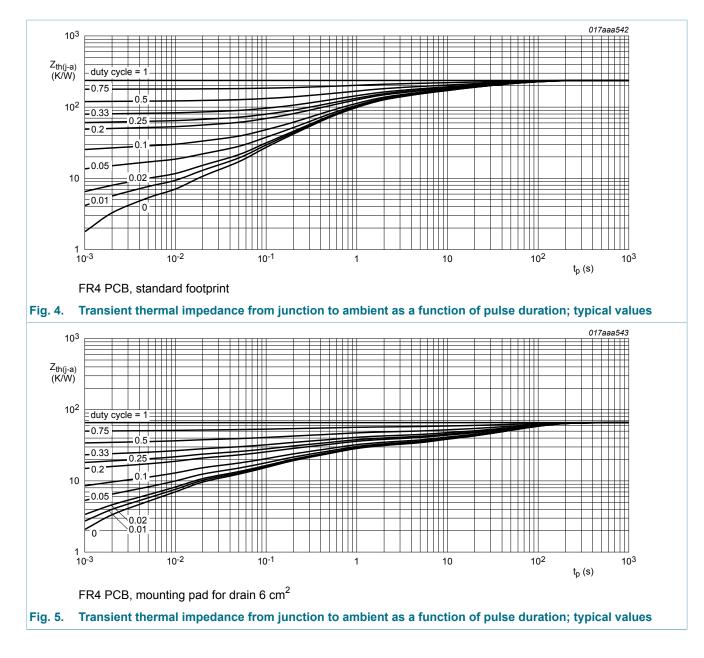
Table 6. Thermal characteristics							
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
ui(j-a)	thermal resistance	in free air	[1]	-	235	270	K/W
	from junction to		[2]	-	67	74	K/W
	ampient	in free air; t ≤ 5 s	[2]	-	33	36	K/W
R <sub>th(j-sp)</sub>	thermal resistance from junction to solder point			-	5	10	K/W

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for drain 6 cm<sup>2</sup>.

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## 7. Characteristics

Table 7. Cl	haracteristics						
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
Static characteristics							
V <sub>(BR)DSS</sub>	drain-source breakdown voltage	$I_D$ = -250 µA; $V_{GS}$ = 0 V; $T_j$ = 25 °C		-20	-	-	V
V <sub>GSth</sub>	gate-source threshold voltage	I <sub>D</sub> = -250 μA; V <sub>DS</sub> = V <sub>GS</sub> ; T <sub>j</sub> = 25 °C		-0.47	-0.68	-0.9	V
I <sub>DSS</sub>	drain leakage current	$V_{DS}$ = -20 V; $V_{GS}$ = 0 V; $T_j$ = 25 °C		-	-	-1	μA
I <sub>GSS</sub>	gate leakage current	V <sub>GS</sub> = -12 V; V <sub>DS</sub> = 0 V; T <sub>j</sub> = 25 °C		-	-	-100	nA
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Symbol	Parameter	Conditions	Min	Тур	Max	Unit
		$V_{GS}$ = 12 V; $V_{DS}$ = 0 V; $T_j$ = 25 °C	-	-	100	nA
R <sub>DSon</sub>	drain-source on-state	$V_{GS}$ = -4.5 V; I <sub>D</sub> = -7.2 A; T <sub>j</sub> = 25 °C	-	19	22.5	mΩ
	resistance	V <sub>GS</sub> = -4.5 V; I <sub>D</sub> = -7.2 A; T <sub>j</sub> = 150 °C	-	27	33	mΩ
		V <sub>GS</sub> = -2.5 V; I <sub>D</sub> = -6.6 A; T <sub>j</sub> = 25 °C	-	21	27	mΩ
		$V_{GS}$ = -1.8 V; I <sub>D</sub> = -3.9 A; T <sub>j</sub> = 25 °C	-	27	38	mΩ
9 <sub>fs</sub>	forward transconductance	V <sub>DS</sub> = -10 V; I <sub>D</sub> = -7.2 A; T <sub>j</sub> = 25 °C	-	50	-	S
R <sub>G</sub>	gate resistance	f = 1 MHz	-	3.9	-	Ω
Dynamic c	haracteristics	1 1				
Q <sub>G(tot)</sub>	total gate charge	$V_{DS}$ = -10 V; I <sub>D</sub> = -7.2 A; V <sub>GS</sub> = -4.5 V;	-	28.8	43.2	nC
Q <sub>GS</sub>	gate-source charge	T <sub>j</sub> = 25 °C	-	4	-	nC
Q <sub>GD</sub>	gate-drain charge		-	5.8	-	nC
C <sub>iss</sub>	input capacitance	$V_{DS}$ = -10 V; f = 1 MHz; $V_{GS}$ = 0 V;	-	2890	-	pF
C <sub>oss</sub>	output capacitance	T <sub>j</sub> = 25 °C	-	250	-	pF
C <sub>rss</sub>	reverse transfer capacitance		-	225	-	pF
t <sub>d(on)</sub>	turn-on delay time	$V_{DS}$ = -10 V; I <sub>D</sub> = -7.2 A; V <sub>GS</sub> = -4.5 V;	-	16	-	ns
t <sub>r</sub>	rise time	$R_{G(ext)} = 6 \Omega; T_j = 25 °C$	-	54	-	ns
t <sub>d(off)</sub>	turn-off delay time		-	43	-	ns
t <sub>f</sub>	fall time		_	36	-	ns

#### Source-drain diode

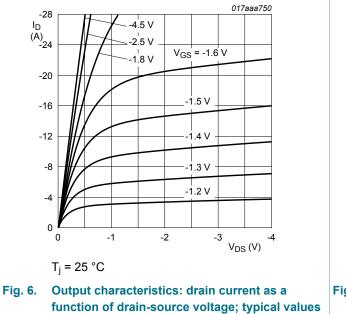
 $V_{SD}$ 

I<sub>S</sub> = -1.9 A; V<sub>GS</sub> = 0 V; T<sub>j</sub> = 25 °C

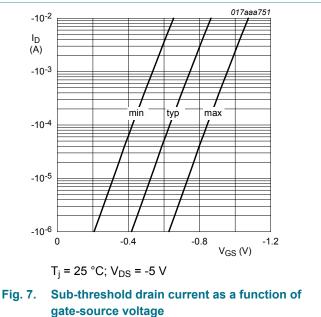
-1.2

V

-0.6



source-drain voltage



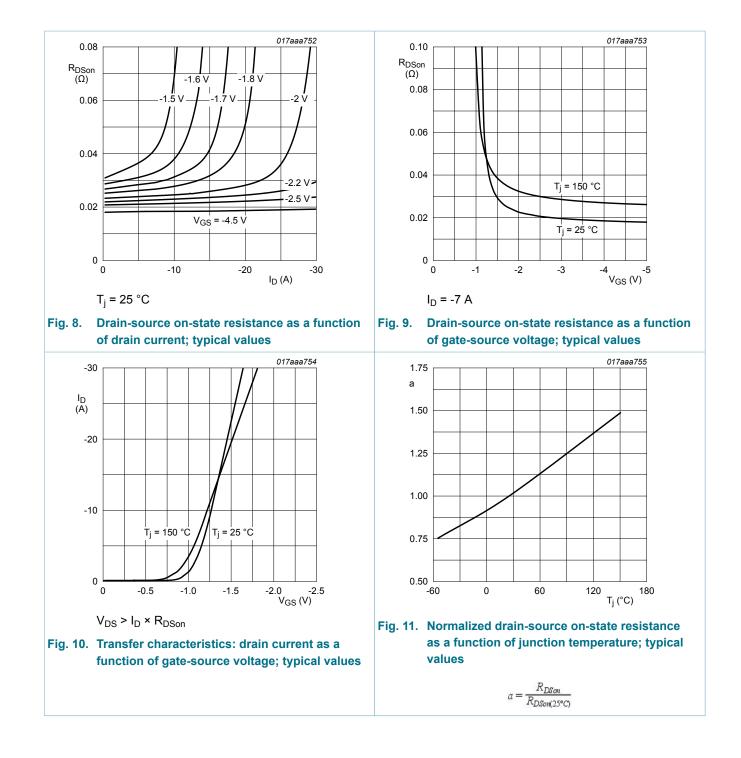
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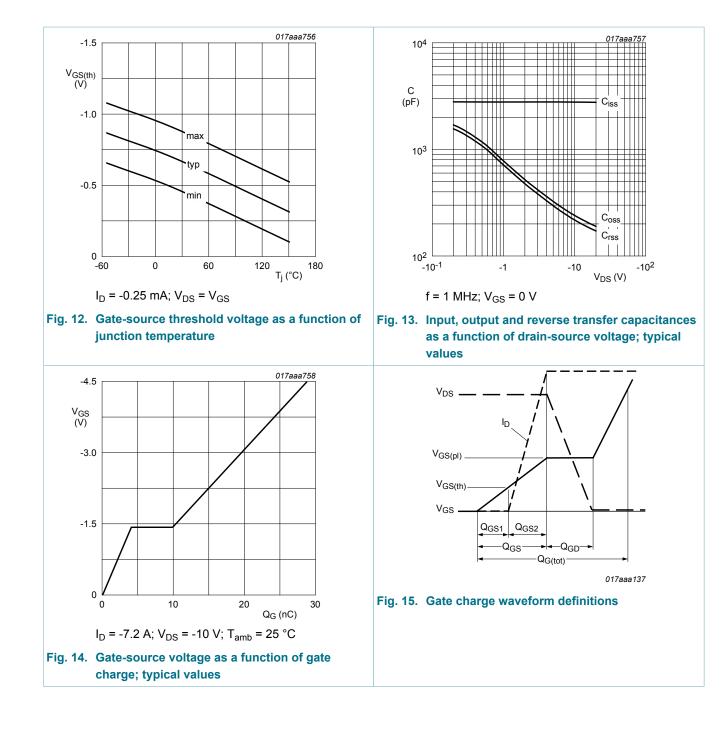
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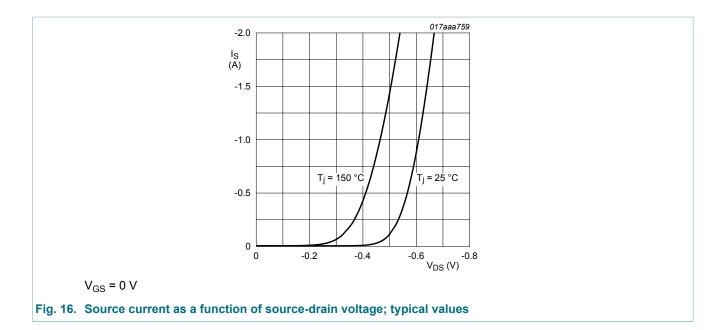
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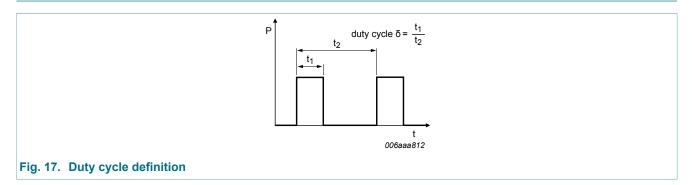


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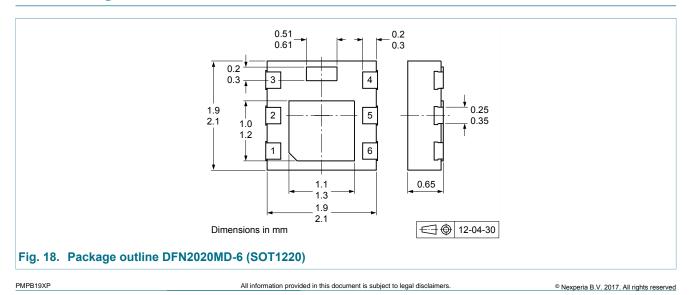
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## 8. Test information

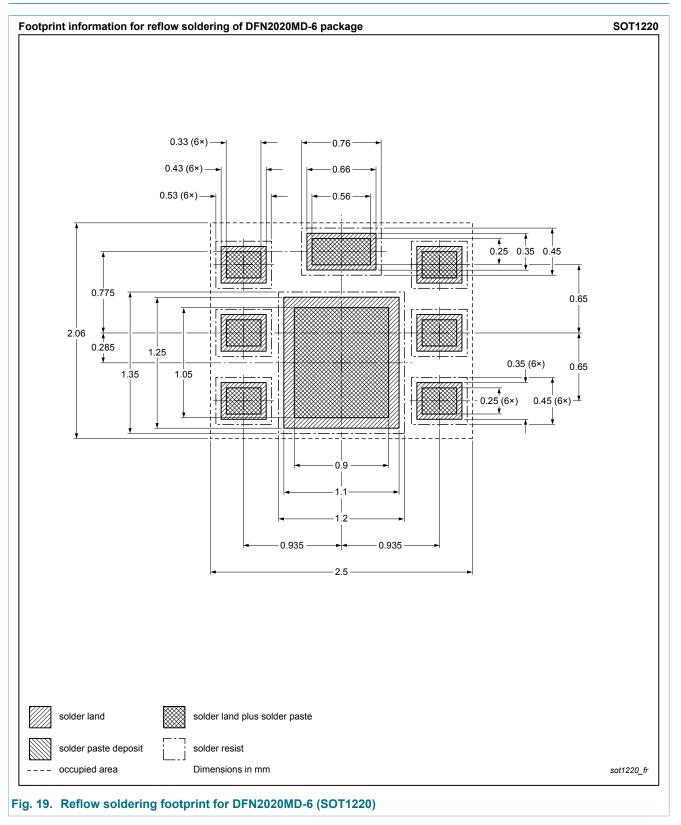


## 9. Package outline



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## **10. Soldering**



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## **11. Revision history**

Table 8. Revision history					
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes	
PMPB19XP v.1	20120920	Product data sheet	-	-	

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#### 12. Legal information

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Document status [1][2]	Product status [ <u>3]</u>	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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