

PMEG4010EH

40 V, 1 A very low VF Schottky barrier rectifier

1 October 2022

Product data sheet

1. General description

Planar Schottky barrier rectifiers with an integrated guard ring for stress protection, encapsulated in a SOD123F small Surface-Mounted Device (SMD) plastic package.

2. Features and benefits

- Forward current: $I_F \le 1 A$
- Reverse voltage: V_R ≤ 40 V
- Very low forward voltage
- AEC-Q101 qualified

3. Applications

- Low voltage rectification
- High efficiency DC-to-DC conversion
- Switch mode power supply
- Reverse polarity protection
- Low power consumption applications

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Symbol	Falametei	Conditions		тур	IVIAX	Unit
I _F	forward current	T _{sp} ≤ 55 °C	-	-	1	А
V _R	reverse voltage		-	-	40	V
V _F	forward voltage	$ I_F = 1 \text{ A}; t_p \le 300 \mu\text{s}; \delta \le 0.02; $	-	540	640	mV
I _R	reverse current	V _R = 40 V; T _{amb} = 25 °C	-	30	100	μA

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	К	cathode	1 2	к ј А
2	A	anode	SOD123F	sym001



6. Ordering information

Table 3. Ordering information						
Type number	pe number Package					
	Name	Description	Version			
PMEG4010EH		plastic, surface-mounted package; 2 leads; 2.6 mm x 1.6 mm x 1.1 mm body	SOD123F			

7. Marking

Table 4. Marking codes			
Type number	Marking code		
PMEG4010EH	AB		

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V _R	reverse voltage			-	40	V
l _F	forward current	T _{sp} ≤ 55 °C		-	1	А
I _{FRM}	repetitive peak forward current	t _p ≤ 1 ms; δ ≤ 0.25		-	7	A
I _{FSM}	non-repetitive peak forward current	t _p = 8 ms; square wave		-	9	A
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1] [2]	-	375	mW
			[3] [2]	-	830	mW
Tj	junction temperature			-	150	°C
T _{amb}	ambient temperature			-65	150	°C
T _{stg}	storage temperature			-65	150	°C

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

[2] Reflow soldering is the only recommended soldering method.

[3] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm².

9. Thermal characteristics

Table 6. The	Table 6. Thermal characteristics						
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
R _{th(j-a)}	thermal resistance from junction to ambient		[1] [2] [3]	-	-	330	K/W
			[4] [2] [3]	-	-	150	K/W

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
R _{th(j-sp)}	thermal resistance from junction to solder point		[5]	-	-	60	K/W

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[2] Reflow soldering is the only recommended soldering method.

[3] For Schottky barrier diodes thermal runaway has to be considered, as in some applications the reverse power losses P_R are a significant part of the total power losses.

[4] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm².

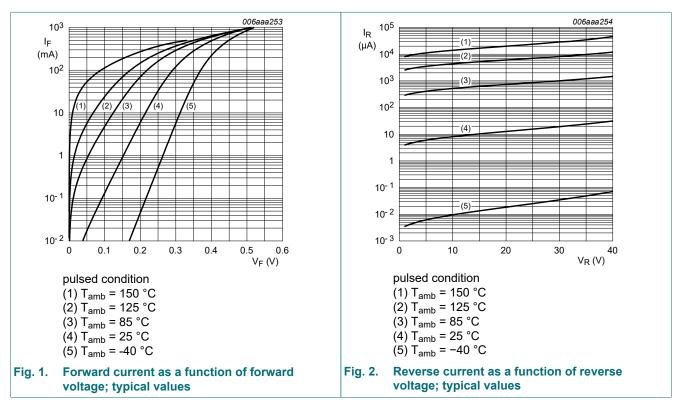
[5] Soldering point of cathode tab.

10. Characteristics

Table 7. Charac	teristics
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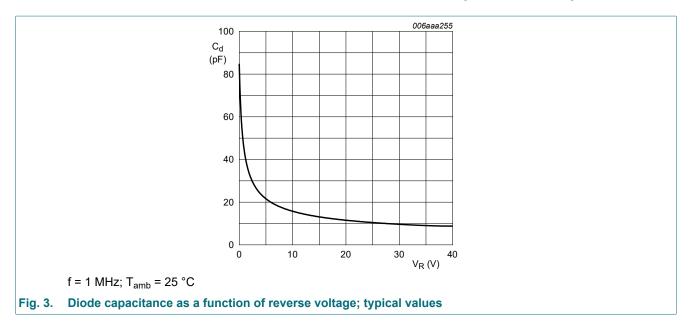
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Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _F	forward voltage	$I_F = 0.1 \text{ mA}; t_p \le 300 \text{ μs}; \delta \le 0.02;$ $T_{amb} = 25 \text{ °C}$	-	95	130	mV
		I_F = 1 mA; $t_p \le 300 \ \mu$ s; $\delta \le 0.02$; T_{amb} = 25 °C	-	155	210	mV
		$ I_F = 10 \text{ mA}; t_p \le 300 \mu\text{s}; \delta \le 0.02; $	-	220	270	mV
		I_F = 100 mA; t _p ≤ 300 μs; δ ≤ 0.02; T _{amb} = 25 °C	-	295	350	mV
		I_F = 500 mA; t _p ≤ 300 μs; δ ≤ 0.02; T _{amb} = 25 °C	-	420	470	mV
		I_F = 1 A; t _p ≤ 300 μs; δ ≤ 0.02; T _{amb} = 25 °C	-	540	640	mV
I _R	reverse current	V _R = 10 V; T _{amb} = 25 °C	-	7	20	μA
		V _R = 40 V; T _{amb} = 25 °C	-	30	100	μA
C _d	diode capacitance	V _R = 1 V; f = 1 MHz; T _{amb} = 25 °C	-	43	50	pF

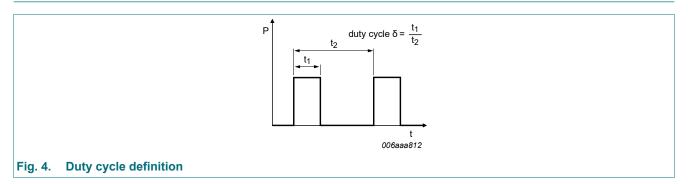


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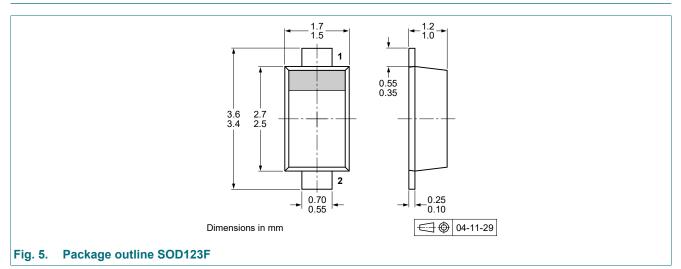
11. Test information



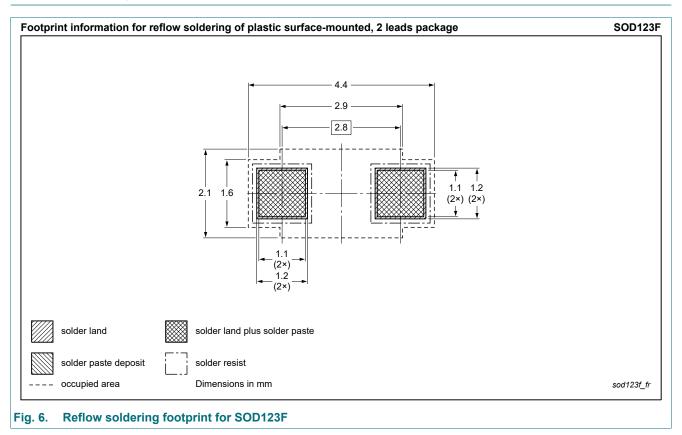
Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - Stress test qualification for discrete semiconductors, and is suitable for use in automotive applications.

12. Package outline



13. Soldering



14. Revision history

Table 8. Revision history						
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes		
PMEG4010EH v.5	20221001	Product data sheet	-	PMEG4010EH_EJ_ET_4		
Modifications:	-	Family data sheet reduced to single type data sheets.Packing information removed.				
PMEG4010EH_EJ_ET_4	20070321	Product data sheet	-	PMEGXX10EH_EJ_SER_3		
PMEGXX10EH_EJ_SER_3	20050411	Product data sheet	-	PMEGXX10EJ_SER_2		
PMEGXX10EJ_SER_2	20050131	Product data sheet	-	PMEGXX10EJ_SER_1		
PMEGXX10EJ_SER_1	20040907	Objective data sheet	-	-		

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

 Please consult the most recently issued document before initiating or completing a design.

- [2] The term 'short data sheet' is explained in section "Definitions".
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