## 1. General description

Planar Maximum Efficiency General Application (MEGA) Schottky barrier rectifier with an integrated guard ring for stress protection, encapsulated in a leadless ultra small SOD1608 (DFN1608D-2) Surface-Mounted Device (SMD) plastic package with visible and solderable side pads.

### 2. Features and benefits

- Average forward current: I<sub>F(AV)</sub> ≤ 0.5 A
- Reverse voltage: V<sub>R</sub> ≤ 40 V
- Low forward voltage V<sub>F</sub> ≤ 590 mV
- Low reverse current
- · Solderable side pads
- · Package height typ. 0.37 mm
- · Ultra small and leadless SMD plastic package
- AEC-Q101 qualified

# 3. Applications

- · Low voltage rectification
- High efficiency DC-to-DC conversion
- · Switch mode power supply
- LED backlight for mobile application
- Low power consumption applications
- · Ultra high-speed switching
- Reverse polarity protection

### 4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
I <sub>F(AV)</sub>	average forward current	$\delta$ = 0.5; square wave; f = 20 kHz; T <sub>amb</sub> $\leq$ 115 °C	[1]	-	-	0.5	А
		$\delta$ = 0.5; square wave; f = 20 kHz; T <sub>sp</sub> ≤ 140 °C		-	-	0.5	А
V <sub>R</sub>	reverse voltage	T <sub>j</sub> = 25 °C		-	-	40	V
V <sub>F</sub>	forward voltage	$I_F$ = 500 mA; $t_p$ ≤ 300 μs; δ ≤ 0.02; pulsed; $T_j$ = 25 °C		-	530	590	mV
I <sub>R</sub>	reverse current	V <sub>R</sub> = 10 V; T <sub>j</sub> = 25 °C		-	0.4	2	μΑ
t <sub>rr</sub>	reverse recovery time	$I_F = 0.5 \text{ A}; I_R = 0.5 \text{ A}; I_{R(meas)} = 0.1 \text{ A};$ $T_j = 25 \text{ °C}$		-	2	-	ns

[1] Device mounted on a ceramic Printed-Circuit Board (PCB), Al<sub>2</sub>O<sub>3</sub>, standard footprint.



# 5. Pinning information

#### **Table 2. Pinning information**

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	K	cathode[1]		K <b>_}</b> A
2	A	anode	1 2	sym001
			Transparent top view DFN1608D-2 (SOD1608)	

[1] The marking bar indicates the cathode.

# 6. Ordering information

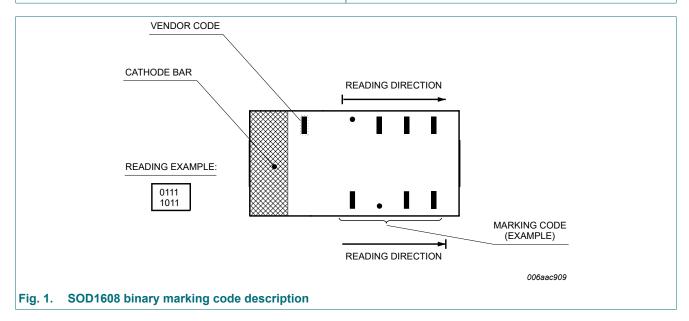
**Table 3. Ordering information** 

Type number	Package				
	Name	Description	Version		
PMEG4005EPK		plastic, leadless ultra small plastic package with side-wettable flanks (SWF); 2 terminals; 0.94 mm pitch; 1.6 mm x 0.8 mm x 0.37 mm body	SOD1608		

# 7. Marking

#### Table 4. Marking codes

Type number	Marking code
PMEG4005EPK	0010 0000



# 8. Limiting values

#### **Table 5. Limiting values**

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V <sub>R</sub>	reverse voltage	T <sub>j</sub> = 25 °C		-	40	V
l <sub>F</sub>	forward current	T <sub>sp</sub> ≤ 135 °C		-	0.7	Α
I <sub>F(AV)</sub>	average forward current	$\delta$ = 0.5; square wave; f = 20 kHz; $T_{amb} \le$ 115 °C	[1]	-	0.5	А
		$\delta$ = 0.5; square wave; f = 20 kHz; $T_{sp} \le$ 140 °C		-	0.5	А
I <sub>FRM</sub>	repetitive peak forward current	$t_p \le 1 \text{ ms}; \delta \le 0.25$		-	2	Α
I <sub>FSM</sub>	non-repetitive peak forward current	$t_p$ = 8 ms; square wave; $T_{j(init)}$ = 25 °C		-	3	А
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C	[2] [3]	-	390	mW
			[4] [3]	-	830	mW
			[1] [3]	-	1470	mW
T <sub>j</sub>	junction temperature			-	150	°C
T <sub>amb</sub>	ambient temperature			-55	150	°C
T <sub>stg</sub>	storage temperature			-65	150	°C

- [1] Device mounted on a ceramic Printed-Circuit Board (PCB), Al<sub>2</sub>O<sub>3</sub>, standard footprint.
- [2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
- [3] Reflow soldering is the only recommended soldering method.
- [4] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm<sup>2</sup>.

### 9. Thermal characteristics

**Table 6. Thermal characteristics** 

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
R <sub>th(j-a)</sub>	thermal resistance from junction to ambient	in free air	[1] [2] [3]	-	-	320	K/W
			[1] [4] [3]	-	-	150	K/W
			[1] [5] [3]	-	-	85	K/W
R <sub>th(j-sp)</sub>	thermal resistance from junction to solder point		[6]	-	-	20	K/W

<sup>[1]</sup> For Schottky barrier diodes thermal runaway has to be considered, as in some applications the reverse power losses P<sub>R</sub> are a significant part of the total power losses.

<sup>[2]</sup> Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

<sup>[3]</sup> Reflow soldering is the only recommended soldering method.

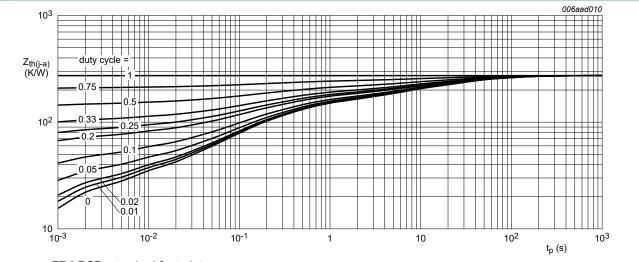
<sup>[4]</sup> Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm<sup>2</sup>.

<sup>[5]</sup> Device mounted on a ceramic PCB, Al<sub>2</sub>O<sub>3</sub>, standard footprint.

<sup>[6]</sup> Soldering point of cathode tab.

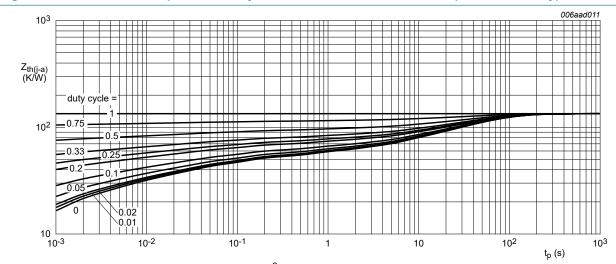
Nexperia PMEG4005EPK

#### 40 V, 0.5 A low VF MEGA Schottky barrier rectifier



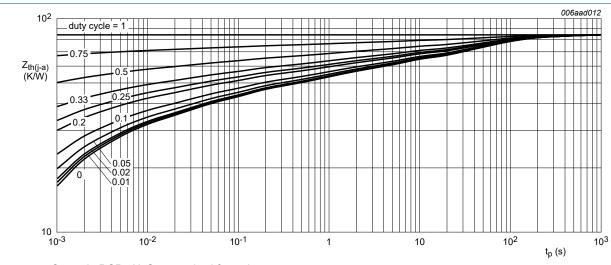
FR4 PCB, standard footprint

Fig. 2. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values



FR4 PCB, mounting pad for cathode 1 cm<sup>2</sup>

Fig. 3. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values



Ceramic PCB, Al<sub>2</sub>O<sub>3</sub>, standard footprint

Fig. 4. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

## 10. Characteristics

**Table 7. Characteristics** 

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V <sub>F</sub>	forward voltage	$I_F$ = 100 mA; $t_p \le 300$ μs; $δ \le 0.02$ ; pulsed; $T_j$ = 25 °C	-	380	420	mV
		$I_F$ = 500 mA; $t_p \le 300$ μs; $δ \le 0.02$ ; pulsed; $T_j$ = 25 °C	-	530	590	mV
I <sub>R</sub>	reverse current	V <sub>R</sub> = 10 V; T <sub>j</sub> = 25 °C	-	0.4	2	μA
		V <sub>R</sub> = 40 V; T <sub>j</sub> = 25 °C	-	2	10	μA
C <sub>d</sub>	diode capacitance	V <sub>R</sub> = 1 V; f = 1 MHz; T <sub>j</sub> = 25 °C	-	30	35	pF
		V <sub>R</sub> = 10 V; f = 1 MHz; T <sub>j</sub> = 25 °C	-	10	15	pF
t <sub>rr</sub>	reverse recovery time	$I_F = 0.5 \text{ A}$ ; $I_R = 0.5 \text{ A}$ ; $I_{R(meas)} = 0.1 \text{ A}$ ; $I_{j} = 25 \text{ °C}$	-	2	-	ns
$V_{FRM}$	peak forward recovery voltage	$I_F = 0.5 \text{ A}; dI_F/dt = 20 \text{ A/}\mu\text{s}; T_j = 25 \text{ °C}$	-	545	-	mV

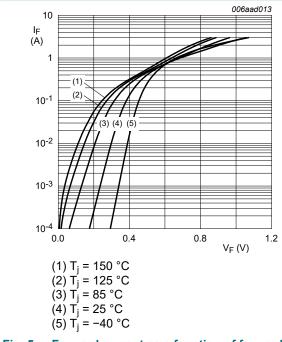


Fig. 5. Forward current as a function of forward voltage; typical values

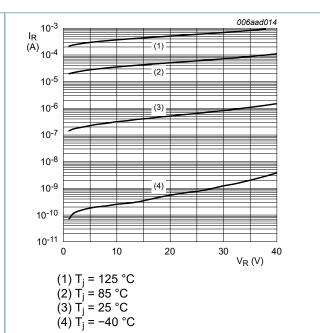


Fig. 6. Reverse current as a function of reverse voltage; typical values

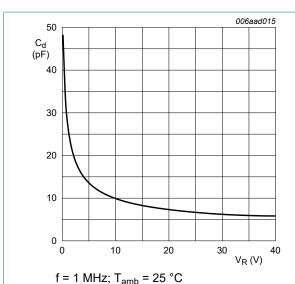
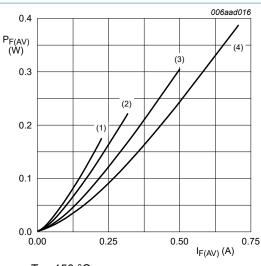
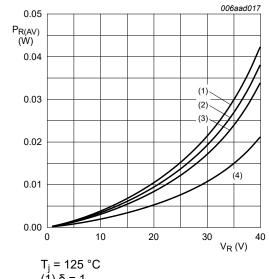


Fig. 7. Diode capacitance as a function of reverse voltage; typical values



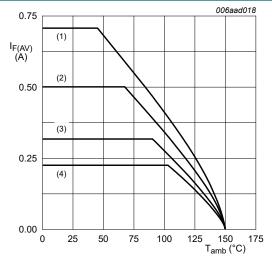
 $T_i = 150 \, ^{\circ}C$  $(1) \delta = 0.1$  $(2) \delta = 0.2$  $(3) \delta = 0.5$  $(4) \delta = 1$ 

Average forward power dissipation as a function of average forward current; typical values



 $(1) \delta = 1$  $(2) \delta = 0.9$  $(3) \delta = 0.8$  $(4) \delta = 0.5$ 

Fig. 9. Average reverse power dissipation as a function of reverse voltage; typical values



FR4 PCB, standard footprint T<sub>i</sub> = 150 °C  $(1) \delta = 1 (DC)$ 

(2)  $\delta = 0.5$ ; f = 20 kHz

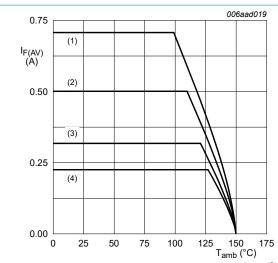
(3)  $\delta = 0.2$ ; f = 20 kHz

(4)  $\delta = 0.1$ ; f = 20 kHz

Fig. 10. Average forward current as a function of ambient temperature; typical values

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FR4 PCB, mounting pad for cathode 1  $\mathrm{cm}^2$ 

T<sub>i</sub> = 150 °C

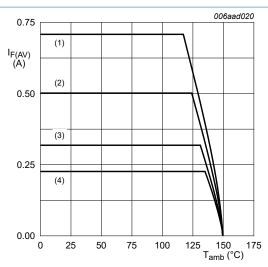
 $(1) \delta = 1 (DC)$ 

(2)  $\delta = 0.5$ ; f = 20 kHz

(3)  $\delta = 0.2$ ; f = 20 kHz

(4)  $\delta = 0.1$ ; f = 20 kHz

Fig. 11. Average forward current as a function of ambient temperature; typical values



Ceramic PCB, Al<sub>2</sub>O<sub>3</sub>, standard footprint

T<sub>i</sub> = 150 °C

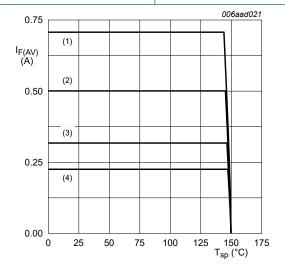
 $(1) \delta = 1 (DC)$ 

(2)  $\delta = 0.5$ ; f = 20 kHz

(3)  $\delta = 0.2$ ; f = 20 kHz

(4)  $\delta = 0.1$ ; f = 20 kHz

Fig. 12. Average forward current as a function of ambient temperature; typical values



T<sub>i</sub> = 150 °C

 $(1) \delta = 1 (DC)$ 

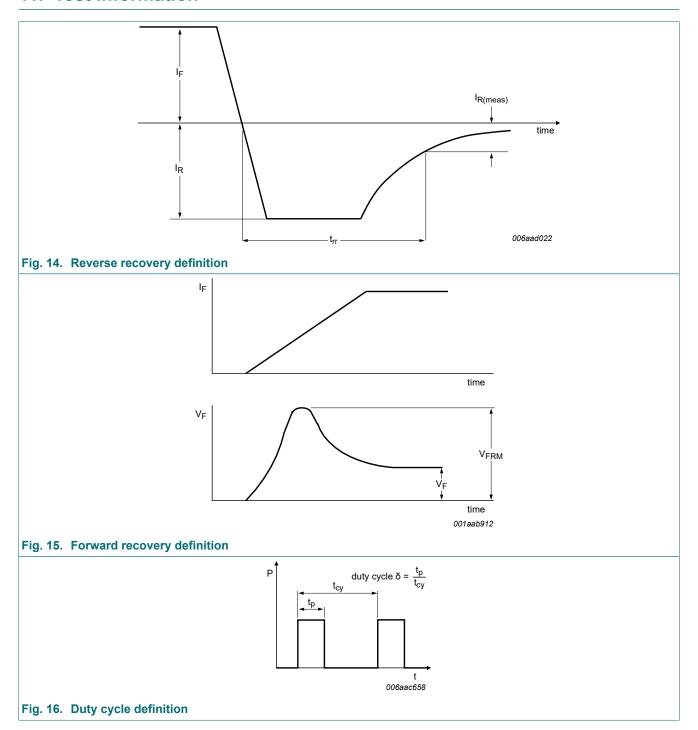
(2)  $\delta = 0.5$ ; f = 20 kHz

 $(3) \delta = 0.2$ ; f = 20 kHz

(4)  $\delta = 0.1$ ; f = 20 kHz

Fig. 13. Average forward current as a function of solder point temperature; typical values

## 11. Test information

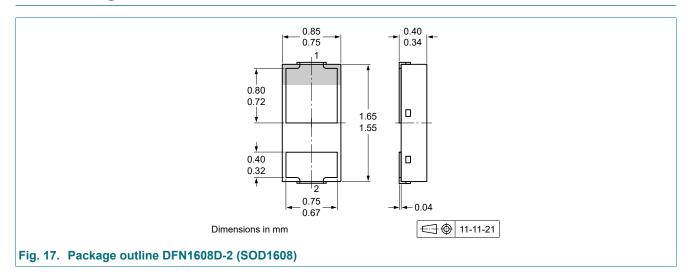


The current ratings for the typical waveforms are calculated according to the equations:  $I_{F(AV)} = I_M \times \delta$  with  $I_M$  defined as peak current,  $I_{RMS} = I_{F(AV)}$  at DC, and  $I_{RMS} = I_M \times \sqrt{\delta}$  with  $I_{RMS}$  defined as RMS current.

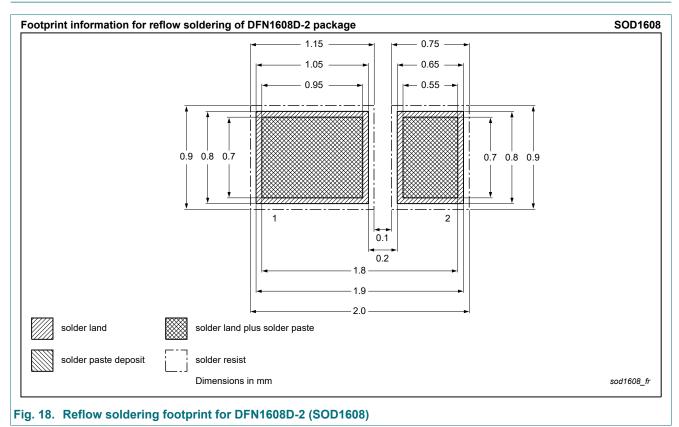
#### **Quality information**

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - Stress test qualification for discrete semiconductors, and is suitable for use in automotive applications.

# 12. Package outline



# 13. Soldering



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# 14. Revision history

### **Table 8. Revision history**

Table 0. Revision misto	' y					
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes		
PMEG4005EPK v.3	20200430	Product data sheet	-	PMEG4005EPK v.2		
Modifications:	<ul> <li>The format of this data sheet has been redesigned to comply with the identity guidelines of Nexperia.</li> <li>Legal texts have been adapted to the new company name where appropriate.</li> </ul>					
PMEG4005EPK v.2	20120306	Product data sheet	-	PMEG4005EPK v.1		
PMEG4005EPK v.1	20120306	Product data sheet	-	-		

## 15. Legal information

#### **Data sheet status**

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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