Product data sheet

1. General description

NPN low V_{CEsat} double transistor in a SOT666 ultra small and flat lead Surface-Mounted Device (SMD) plastic package.

PNP complement: PBSS3515VS

2. Features and benefits

- 300 mW total power dissipation
- Very small 1.6 x 1.2 mm ultra thin package
- Excellent coplanarity due to straight leads
- Low collector-emitter saturation voltage
- High current capability
- Improved thermal behavior due to flat lead
- Replaces two SC-75/SC-89 packaged low V_{CEsat} transistors on same PCB area
- Reduces required PCB area
- Reduced pick and place costs

3. Applications

- General purpose switching and muting
- Low frequency driver circuits
- · Audio frequency general purpose amplifier applications
- · Battery driven equipment (mobile phones, video cameras and hand-held devices)

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Per transistor						
V _{CEO}	collector-emitter voltage	open base	-	-	15	V
I _C	collector current		-	-	500	mA
h _{FE}	DC current gain	V _{CE} = 2 V; I _C = 10 mA; T _{amb} = 25 °C	200	-	-	



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5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	E1	emitter TR1	6 5 4	C1 B2 E2
2	B1	base TR1		
3	C2	collector TR2		(TR1 TR2)
4	E2	emitter TR2		
5	B2	base TR2	1 2 3	
6	C1	collector TR1	SOT666	sym020

6. Ordering information

Table 3. Ordering information

Type number Package						
	Name	Description	Version			
PBSS2515VS		plastic, surface-mounted package; 6 leads; 0.5 mm pitch; 1.6 mm x 1.2 mm x 0.55 mm body	<u>SOT666</u>			

7. Marking

Table 4. Marking codes

Type number	Marking code
PBSS2515VS	N9

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
Per transiste	or		•		•	
V _{CBO}	collector-base voltage	open emitter		-	15	V
V _{CEO}	collector-emitter voltage	open base		-	15	V
V _{EBO}	emitter-base voltage	open collector		-	6	V
I _C	collector current			-	500	mA
I _{CM}	peak collector current	single pulse; t _p ≤ 1 ms		-	1	Α
I _{BM}	peak base current			-	100	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	200	mW
Per device			,	'	•	
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	300	mW
Tj	junction temperature			-	150	°C
T _{amb}	ambient temperature			-65	150	°C
T _{stg}	storage temperature			-65	150	°C

^[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

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9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
Per device							
R _{th(j-a)}	thermal resistance from junction to ambient		[1] [2]	-	-	416	K/W

^[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions	M	in 1	Гур	Max	Unit
Per transist	tor						
I _{CBO}	collector-base cut-off	V _{CB} = 15 V; I _E = 0 A; T _{amb} = 25 °C	-	-		100	nA
	current	V _{CB} = 15 V; I _E = 0 A; T _j = 150 °C	-	-		50	μA
I _{EBO}	emitter-base cut-off current	V _{EB} = 5 V; I _C = 0 A; T _{amb} = 25 °C	-	-		100	nA
h _{FE}	DC current gain	V _{CE} = 2 V; I _C = 10 mA; T _{amb} = 25 °C	20	00 -	•	-	
		V_{CE} = 2 V; I_{C} = 100 mA; pulsed; t_{p} ≤ 300 μs; δ ≤ 0.02; T_{amb} = 25 °C	1	50 -	•	-	
		V_{CE} = 2 V; I_{C} = 500 mA; pulsed; $t_{p} \le$ 300 μs; $\delta \le$ 0.02; T_{amb} = 25 °C	90) -		-	
V _{CEsat}	collector-emitter	I _C = 10 mA; I _B = 0.5 mA; T _{amb} = 25 °C	-	-		25	mV
	saturation voltage	I _C = 200 mA; I _B = 10 mA; T _{amb} = 25 °C	-	-		150	mV
		I_C = 500 mA; I_B = 50 mA; pulsed; $t_p \le$	-	-		250	mV
R _{CEsat}	collector-emitter saturation resistance	300 μs; δ ≤ 0.02; T _{amb} = 25 °C	-	3	300	500	mΩ
V _{BEsat}	base-emitter saturation voltage		-	-	•	1.1	V
V_{BEon}	base-emitter turn-on voltage	V_{CE} = 2 V; I_{C} = 100 mA; pulsed; $t_{p} \le$ 300 μs; $\delta \le$ 0.02; T_{amb} = 25 °C	-	-	•	0.9	V
f _T	transition frequency	$V_{CE} = 5 \text{ V}; I_{C} = 100 \text{ mA}; f = 100 \text{ MHz};$ $T_{amb} = 25 \text{ °C}$	2	50 4	120	-	MHz
C _c	collector capacitance	V_{CB} = 10 V; I_{E} = 0 A; i_{e} = 0 A; f = 1 MHz; T_{amb} = 25 °C	-	4	1.4	6	pF

^[2] The only recommended soldering method is reflow soldering.

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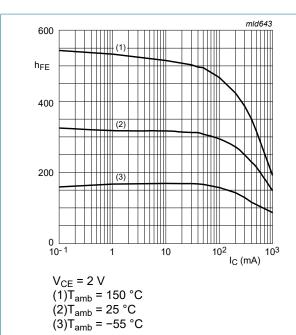


Fig. 1. DC current gain as a function of collector current; typical values

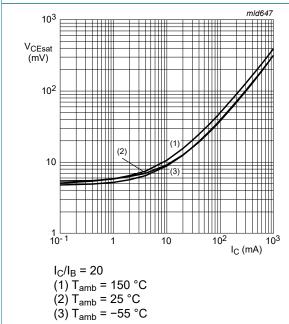


Fig. 3. Collector-emitter saturation voltage as a function of collector current; typical values.

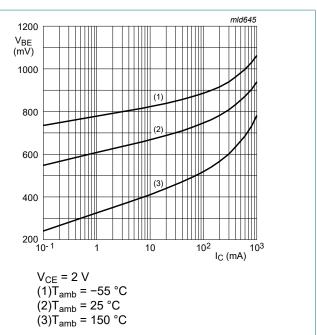
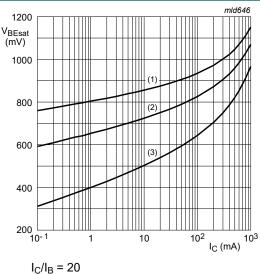


Fig. 2. Base-emitter voltage as a function of collector current; typical values.



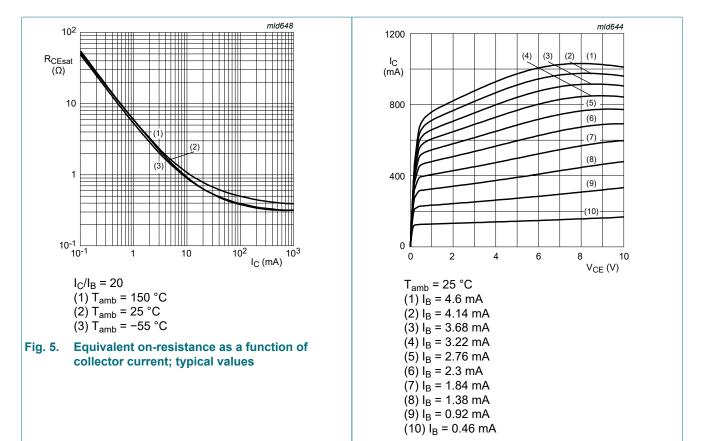
 $I_C/I_B = 20$ (1) $T_{amb} = 150$ °C (2) $T_{amb} = 25$ °C (3) $T_{amb} = -55$ °C

Fig. 4. Base-emitter saturation voltage as a function of collector current; typical values

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Collector current as a function of collector-

emitter voltage; typical values



11. Package outline

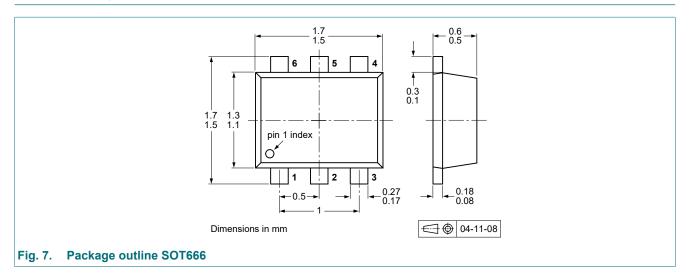
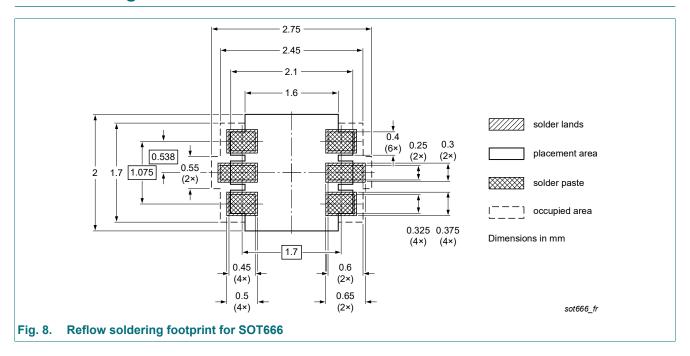


Fig. 6.

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12. Soldering



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13. Revision history

Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes		
PBSS2515VS v.3	20221228	Product data sheet	-	PBSS2515VS v.2		
Modifications:	 The format of this data sheet has been redesigned to comply with the identity guidelines of Nexperia. Legal texts have been adapted to the new company name where appropriate. Product(s) changed to non-automotive qualification. 					
PBSS2515VS v.2	20041223	Product data sheet	-	PBSS2515VS v.1		
PBSS2515VS v.1	20011107	Product data sheet	-	-		

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14. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions".
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