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September 2015

KSA1015 PNP Epitaxial Silicon Transistor

Features

- · Low-Frequency Amplifier
- Collector-Base Voltage: V_{CBO} = -50 V
- · Complement to KSC1815



Ordering Information

Part Number	Marking Package		Packing Method	
KSA1015GRTA	A1015	TO-92 3L	Ammo	
KSA1015YTA	A1015	TO-92 3L	Ammo	

Absolute Maximum Ratings

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at $T_A = 25^{\circ}\text{C}$ unless otherwise noted.

Symbol	Parameter	Value	Unit
V _{CBO}	Collector-Base Voltage	-50	V
V _{CEO}	Collector-Emitter Voltage	-50	V
V _{EBO}	Emitter-Base Voltage	-5	V
I _C	Collector Current	-150	mA
I _B	Base Current	-50	mA
T _J	Junction Temperature	150	°C
T _{STG}	Storage Temperature Range	-55 to 150	°C

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Thermal Characteristics(1)

Values are at $T_A = 25$ °C unless otherwise noted.

Symbol	Parameter	Max.	Unit
В	Total Device Dissipation	400	mW
P_{D}	Derate Above 25°C	3.2	mW/°C
$R_{\theta JA}$	R _{0JA} Thermal Resistance, Junction to Ambient		°C/W

Note:

1. PCB size: FR-4, 76 mm x 114 mm x 1.57 mm (3.0 inch x 4.5 inch x 0.062 inch) with minimum land pattern size.

Electrical Characteristics

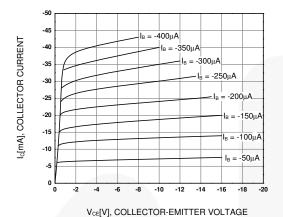
Values are at $T_A = 25$ °C unless otherwise noted.

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV _{CBO}	Collector-Base Breakdown Voltage	$I_C = -100 \mu\text{A}, I_E = 0$	-50			V
BV _{CEO}	Collector-Emitter Breakdown Voltage	$I_C = -10 \text{ mA}, I_B = 0$	-50			V
BV _{EBO}	Emitter-Base Breakdown Voltage	$I_E = -10 \mu A, I_C = 0$	-5			V
I _{CBO}	Collector Cut-Off Current	$V_{CB} = -50 \text{ V}, I_{E} = 0$			-0.1	μΑ
I _{EBO}	Emitter Cut-Off Current	$V_{EB} = -5 \text{ V}, I_{C} = 0$			-0.1	μΑ
h _{FE} 1	DC Current Gain	$V_{CE} = -6 \text{ V}, I_{C} = -2 \text{ mA}$	70		400	
h _{FE} 2	DC Current Gain	$V_{CE} = -6 \text{ V}, I_{C} = -150 \text{ mA}$	25			
V _{CE} (sat)	Collector-Emitter Saturation Voltage	$I_C = -100 \text{ mA}, I_B = -10 \text{ mA}$		-0.1	-0.3	V
V _{BE} (sat)	Base-Emitter Saturation Voltage	$I_C = -100 \text{ mA}, I_B = -10 \text{ mA}$			-1.1	V
f _T	Current Gain Bandwidth Product	$V_{CE} = -10 \text{ V}, I_{C} = -1 \text{ mA}$	80			MHz
C _{ob}	Output Capacitance	$V_{CB} = -10 \text{ V}, I_{E} = 0,$ f = 1 MHz		4	7	pF
NF	Noise Figure	$V_{CE} = -6 \text{ V, } I_{C} = -0.1 \text{ mA,}$ $f = 100 \text{ Hz, } R_{G} = 10 \text{ k}\Omega$		0.5	6	dB

h_{FE} Classification

Classification	0	Υ	GR	
h _{FE} 1	70 ~ 140	120 ~ 240	200 ~ 400	

Typical Performance Characteristics





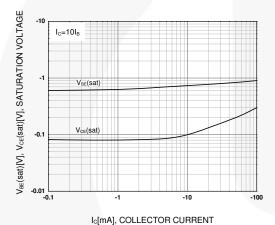


Figure 3. Base-Emitter Saturation Voltage and Collector-Emitter Saturation Voltage

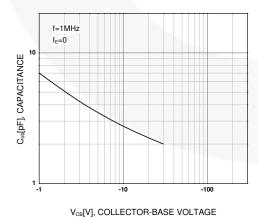


Figure 5. Collector Output Capacitance

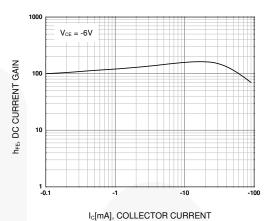


Figure 2. DC Current Gain

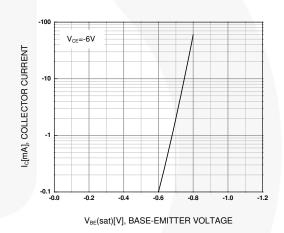


Figure 4. Base-Emitter On Voltage

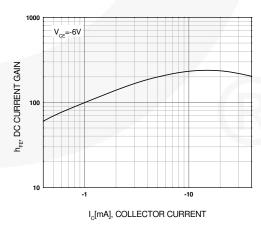


Figure 6. Current Gain Bandwidth Product

Physical Dimensions 3.44 2.54 13.00 10,50 2.80 2.40 NOTES: UNLESS OTHERWISE SPECIFIED DRAWING CONFORMS TO JEDEC MS-013, VARIATION AC. ALL DIMENSIONS ARE IN MILLIMETERS. DRAWING CONFORMS TO ASME Y14.5M-2009. DRAWING FILENAME: MKT-ZA03FREV3. FAIRCHILD SEMICONDUCTOR.

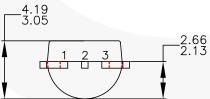


Figure 7. 3-LEAD, TO-92, MOLDED 0.200 IN LINE SPACING LD FORM



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Definition of Terms				
Datasheet Identification	Product Status	Definition		
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Preliminary	First Production	Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.		
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