

Isc N-Channel MOSFET Transistor

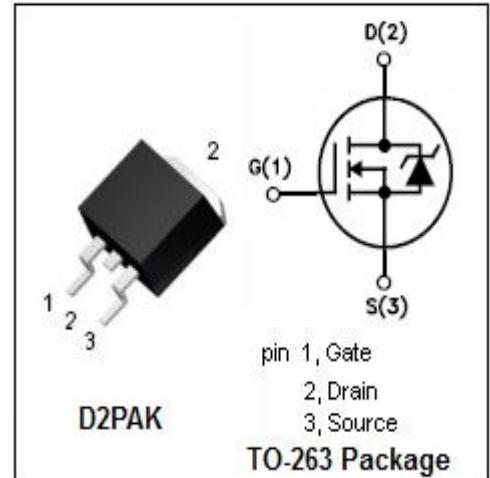
IPB65R310CFD

• FEATURES

- With To-263(D2PAK) package
- Low input capacitance and gate charge
- Low gate input resistance
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

• APPLICATIONS

- Switching applications

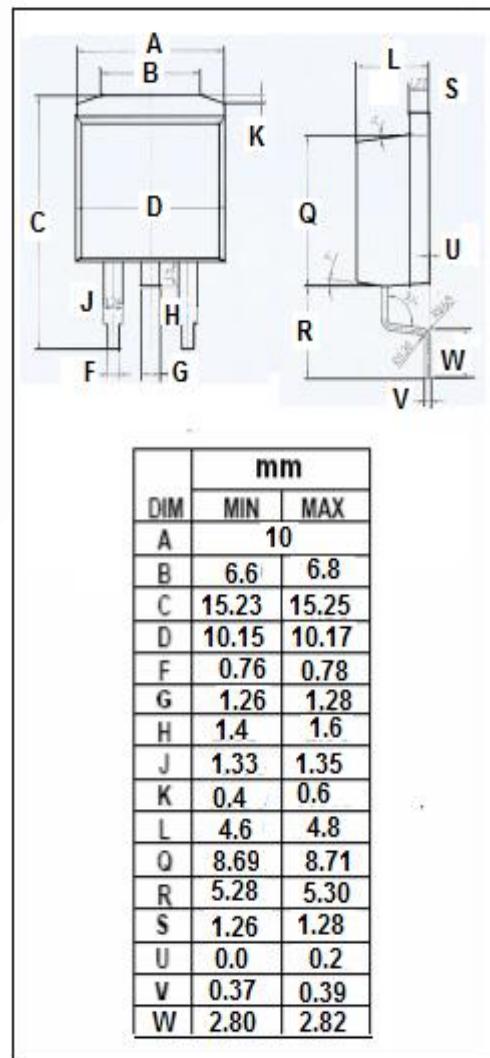


• ABSOLUTE MAXIMUM RATINGS($T_a=25^\circ\text{C}$)

SYMBOL	PARAMETER	VALUE	UNIT
V_{DSS}	Drain-Source Voltage	650	V
V_{GSS}	Gate-Source Voltage	± 30	V
I_D	Drain Current-Continuous $T_c=25^\circ\text{C}$ $T_c=100^\circ\text{C}$	11.4 7.2	A
I_{DM}	Drain Current-Single Pulsed	34.4	A
P_D	Total Dissipation @ $T_c=25^\circ\text{C}$	104.2	W
T_{ch}	Max. Operating Junction Temperature	150	$^\circ\text{C}$
T_{stg}	Storage Temperature	-55~150	$^\circ\text{C}$

• THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	MAX	UNIT
$R_{th(ch-c)}$	Channel-to-case thermal resistance	1.2	$^\circ\text{C}/\text{W}$
$R_{th(ch-a)}$	Channel-to-ambient thermal resistance	62	$^\circ\text{C}/\text{W}$



Isc N-Channel MOSFET Transistor**IPB65R310CFD****ELECTRICAL CHARACTERISTICS** $T_c=25^\circ\text{C}$ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP	MAX	UNIT
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}; I_{\text{D}}=1\text{mA}$	650			V
$V_{\text{GS}(\text{th})}$	Gate Threshold Voltage	$V_{\text{DS}}=V_{\text{GS}}; I_{\text{D}}=0.44\text{mA}$	3.5		4.5	V
$R_{\text{DS}(\text{on})}$	Drain-Source On-Resistance	$V_{\text{GS}}= 10\text{V}; I_{\text{D}}=4.4\text{A}$		280	310	$\text{m}\Omega$
I_{GSS}	Gate-Source Leakage Current	$V_{\text{GS}}= \pm 20\text{V}; V_{\text{DS}}=0\text{V}$			± 0.1	μA
I_{DSS}	Drain-Source Leakage Current	$V_{\text{DS}}=600\text{V}; V_{\text{GS}}= 0\text{V}; T_j=25^\circ\text{C}$ $V_{\text{DS}}=600\text{V}; V_{\text{GS}}= 0\text{V}; T_j=150^\circ\text{C}$			5 600	μA
V_{SDF}	Diode forward voltage	$I_{\text{SD}}=6.6\text{A}, V_{\text{GS}} = 0\text{V}$		0.9		V