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March 2016

# FPF2595 IntelliMAX™ 28 V, Over-Voltage, Over-Current Protection Load Switch with Adjustable Current-Limit Control

#### **Features**

- V<sub>IN</sub>: 2.5 V~5.5 V
- 28 V Absolute Ratings at V<sub>OUT</sub>
- Current Capability: 3.9 A
  - Typ 0.1 A~3.5 A with 10% Accuracy
- R<sub>ON</sub>: Typ. 35 mΩ & Max. 50 mΩ at 5 V<sub>IN</sub> and 1 A I<sub>OUT</sub>
- Output OVP: Min.=5.6 V, Typ.=5.8 V, Max.=6 V
- No Output Discharge During Off State
- Open-Drain OCP on FLAGB
- Thermal Shutdown
- Under-Voltage Lockout (UVLO)
- True Reverse-Current Blocking (TRCB)
- Logic CMOS IO Meets JESD76 Standard for GPIO Interface and Related Power Supply Requirements
- ESD Protected:
  - Human Body Model: >2 kV
  - Charged Device Model: >1.0 kV
  - IEC 61000-4-2 Air Discharge: >15 kV
  - IEC 61000-4-2 Contact Discharge: >8 kV

## **Applications**

- Type C Power Source Switch
- Computing, Monitor
- Portable Devices

## Description

The FPF2595 advanced load-management switch targets applications requiring a highly integrated solution. It disconnects loads powered from the DC power rail (<6 V) with stringent off-state current targets and high load capacitances (<100  $\mu F$ ). The FPF2595 consists of a slew-rate controlled low-impedance MOSFET switch (35 m $\Omega$  typical) and integrated analog features. The slew-rate controlled turn-on characteristic prevents inrush current and the resulting excessive voltage droop on power rails. FPF2595 has over-voltage protection and over-temperature protection.

The FPF2595 has a True Reverse-Current Blocking (TRCB) function that obstructs unwanted reverse current from  $V_{\text{OUT}}$  to  $V_{\text{IN}}$  during ON and OFF states. The exceptionally low off-state current drain (<2  $\mu\text{A}$  maximum) facilitates compliance with standby power requirements. The input voltage range operates from 2.5 V to 5.5  $V_{\text{DC}}$  to support a wide range of applications in consumer, optical, medical, storage, portable, and industrial-device power management. Switch control is managed by a logic input (active HIGH) capable of interfacing directly with low-voltage control signal / General-Purpose Input / Output (GPIO) without an external pull-down resistor.

The device is packaged in advanced, fully "green" compliant, 1.3 mm x 1.8 mm, Wafer-Level Chip-Scale Packages (WLCSP).

## **Ordering Information**

Part Number	Operating Temperature Range	Package	Packing Method	Top Mark
FPF2595UCX	-40 to 85°C	12-Ball, WLCSP, 3x4 Array, 0.4 mm Pitch, 250 µm Ball, Wafer-Level Chip-Scale Package (WLCSP)	Tape & Reel	TY

## **Application Diagram**

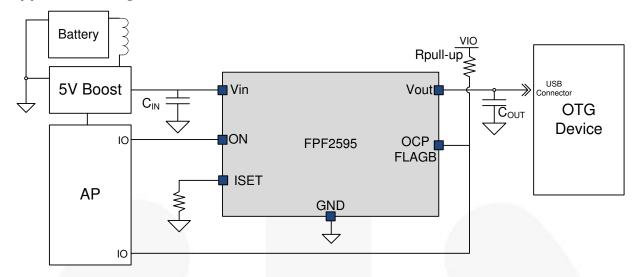


Figure 1. Typical Application

#### Note:

1.  $C_{IN}$  and  $C_{OUT}$  capacitors recommended for improvement of device stability.

## **Functional Block Diagram**

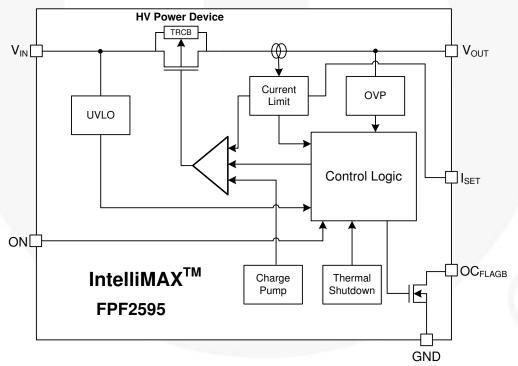


Figure 2. Functional Block Diagram

## **Pin Configurations**

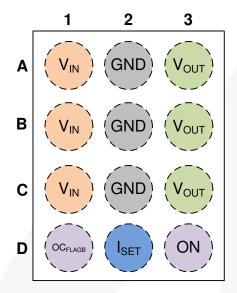


Figure 3. Pin Assignments (Top View)

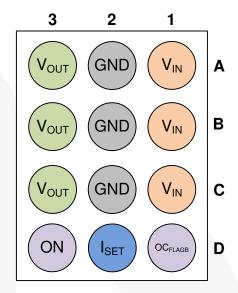


Figure 4. Pin Assignments (Bottom View)

## **Pin Description**

Pin#	Name	Description				
A3, B3, C3	V <sub>OUT</sub>	Switch Output				
A1, B1, C1	$V_{IN}$	Supply Input: Input to the power switch				
A2, B2, C2	GND	Ground (True device ground)				
D3	ON	ON/OFF Control Innut, Active IIICII CDIO competible	Switch Enable			
DЗ	ON	DN/OFF Control Input: Active HIGH - GPIO compatible Logic LOW Switch Disab				
D1	OC <sub>FLAGB</sub>	<b>Fault Output</b> : Active LOW, open-drain output that indicates an input over current. External pull-up resistor to $V_{CC}$ is required.				
D2	I <sub>SET</sub>	Current Limit Set Input: A resistor from ISET to ground sets the current limit for the switch.				

## **Absolute Maximum Ratings**

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only.

Symbol		Parameters		Max.	Unit
V	Vout to GND, Vout to Vin	N	-0.3	28.0	V
$V_{PIN}$	ON, V <sub>IN</sub> , FLAGB, I <sub>SET</sub> to 0	GND	-0.3	6.0	V
I <sub>SW</sub>	Maximum Continuous S	witch Current <sup>(2)</sup>		3.9	Α
t <sub>PD</sub>	Total Power Dissipation	at T <sub>A</sub> =25°C		1.48	W
TJ	Operating Junction Tem	perature	-40	+150	°C
T <sub>STG</sub>	Storage Junction Tempe	erature	-65	+150	°C
$\Theta_{JA}$	Thermal Resistance, Jul (1-inch Square Pad of 2			84.1 <sup>(3)</sup>	°C/W
	Electrostatic Discharge	Human Body Model, JESD22-A114	2.0		
FCD	Capability	Charged Device Model, JESD22-C101	1.0		kV
ESD	IEC61000-4-2 System Air Discharge (V <sub>IN</sub> , V <sub>ON</sub> , V <sub>OUT</sub> to GND)		15.0		ΚV
1	Level	Contact Discharge (V <sub>IN,</sub> V <sub>ON,</sub> V <sub>OUT</sub> to GND)	8.0		

#### Notes:

- 2. Maximum Junction Temperature = 85°C.
- 3. Measured using 2S2P JEDEC std. PCB.

## **Recommended Operating Conditions**

The Recommended Operating Conditions table defines the conditions for actual device operation. Recommended operating conditions are specified to ensure optimal performance to the datasheet specifications. Fairchild does not recommend exceeding them or designing to Absolute Maximum Ratings.

Symbol	Parameters	Min.	Max.	Unit
V <sub>IN</sub>	Supply Voltage	2.5	5.5	٧
T <sub>A</sub>	Ambient Operating Temperature	-40	85	°C

## **Electrical Characteristics**

Unless otherwise noted;  $V_{IN}$ =2.5 to 5.5 V,  $T_A$ =-40 to +85°C; typical values are at  $V_{IN}$ =5 V and  $T_A$ =25°C.

Symbol	Parameters	Parameters Condition Min. Typ.		Тур.	Max.	Unit
Basic Oper	ation					
V <sub>IN</sub>	Input Voltage		2.5		5.5	V
I <sub>Q(OFF)</sub>	Off Supply Current	V <sub>ON</sub> =GND, V <sub>OUT</sub> =Open		1	2	μA
I <sub>SD(OFF)</sub>	Shutdown Current	V <sub>IN</sub> =5.5 V, V <sub>OUT</sub> =0 V, V <sub>ON</sub> =GND		0.1	4.0	μA
IQ	Quiescent Current	I <sub>OUT</sub> =0 mA		65	100	μA
Б	On Basistana	V <sub>IN</sub> =5.0 V, I <sub>OUT</sub> =1 A		35	50	_
R <sub>ON</sub>	On Resistance	V <sub>IN</sub> =3.7 V, I <sub>OUT</sub> =1 A		40	55	mΩ
$V_{IH}$	ON Input Logic HIGH Voltage	V <sub>IN</sub> =2.5 V to 5.5 V	1.15			V
VIL	ON Input Logic LOW Voltage	V <sub>IN</sub> =2.5 V to 5.5 V			0.65	٧
.,	FLAGB Output Logic	V <sub>IN</sub> =5 V, I <sub>SINK</sub> =10 mA	1	0.1	0.2	
$V_{IL_FLAG}$	LOW Voltage	V <sub>IN</sub> =2.5 V, I <sub>SINK</sub> =10 mA		0.15	0.30	V
I <sub>FLAGB_LK</sub>	FLAGB Output HIGH Leakage Current	V <sub>IN</sub> =5 V, Switch On		Y.	1	μA
I <sub>ON</sub>	On Input Leakage	V <sub>ON</sub> =0 V to V <sub>IN</sub>			1.0	μA
R <sub>ON_PD</sub>	Pull-Down Resistance at ON Pin	V <sub>IN</sub> =2.5~5.5 V, V <sub>ON</sub> =HIGH, T <sub>A</sub> =-40 to 85°C		14		ΜΩ
Over-Voltag	ge Protection				I	
.,	0 1 10//01	V <sub>OUT</sub> Rising Threshold	5.50	5.80	6.00	
$V_{OV\_TRIP}$	Output OVP Lockout	V <sub>OUT</sub> Falling Threshold		5.50		V
OUT <sub>HYS</sub>	Output OVP Hysteresis	V <sub>OUT</sub> Falling Threshold		0.3		V
t <sub>OVP</sub>	OVP Response Time <sup>(5)</sup>	$I_{OUT}{=}0.5$ A, $C_L{=}1~\mu\text{F},T_A{=}25^{\circ}\text{C},V_{OUT}$ from $5.5~V$ to $6.0~V$	1	/	4	μs
Over-Curre	nt Protection					
		$V_{IN}$ =5 V, $R_{SET}$ =2300 Ω, $V_{OUT}$ =1.68 to 5 V	450	500	550	
I <sub>LIM</sub>	Current Limit <sup>(4)</sup>	$V_{IN}$ =5 V, $R_{SET}$ =1070 $\Omega$ , $V_{OUT}$ =1.68 to 5 V		1000	1100	mA
M	Llada Valta va Ladrovt	V <sub>IN</sub> Increasing		2.4		
$V_{\text{UVLO}}$	Under-Voltage Lockout	V <sub>IN</sub> Decreasing		2.2		V
V <sub>UVLO_HYS</sub>	UVLO Hysteresis		No.	200		mV
V <sub>T_RCB</sub>	RCB Protection Trip Point	V <sub>OUT</sub> - V <sub>IN</sub>		50		mV
V <sub>R_RCB</sub>	RCB Protection Release Trip Point	V <sub>IN</sub> - V <sub>OUT</sub>		50		mV

Continued on the following page...

## **Electrical Characteristics** (Continued)

Unless otherwise noted;  $V_{IN}$ =2.5 to 5.5 V,  $T_A$ =-40 to +85°C; typical values are at  $V_{IN}$ =5 V and  $T_A$ =25°C.

Symbol	Parameters	Conditions	Min.	Тур.	Max.	Unit
V <sub>RCB_HYS</sub>	RCB Hysteresis			100		mV
t <sub>RCB</sub>	Default RCB Response Time	V <sub>IN</sub> =5 V, V <sub>ON</sub> =High/Low		2		μs
I <sub>RCB</sub>	RCB Current	V <sub>ON</sub> =0 V, V <sub>OUT</sub> =5.5 V,		7		μA
tHOCP	Hard Over-Current Response Time	Moderate Over-Current Condition, I <sub>OUT</sub> ≥ I <sub>LIM</sub> , V <sub>OUT</sub> =0 V		6		μs
t <sub>OCP</sub>	Over-Current Response Time			7		μs
toc_flag	Over-Current Flag Response Time	When Over-Current Occurs to Flag Pulling LOW	A	8		ms
	A**	Shutdown Threshold		150		
TSD	Thermal Shutdown <sup>(5)</sup>	Return from Shutdown		130		°C
	9	Hysteresis		20		
Dynamic C	haracteristics			4		
t <sub>DON</sub>	Turn-On Delay <sup>(5,6)</sup>		1	0.69		ms
t <sub>R</sub>	V <sub>OUT</sub> Rise Time <sup>(5,6)</sup>			0.77		ms
ton	Turn-On Time <sup>(5,7)</sup>	V <sub>IN</sub> =5 V, R <sub>L</sub> =100 Ω, C <sub>L</sub> =1 μF,		1.46		ms
t <sub>DOFF</sub>	Turn-Off Delay <sup>(5,6)</sup>	$T_A=25$ °C, $R_{SET}=2040 \Omega$		10		μs
t <sub>F</sub>	V <sub>OUT</sub> Fall Time <sup>(5,6)</sup>			220		μs
toff	Turn-Off Time <sup>(5,8)</sup>			230		μs

#### Notes:

- 4. Characterization based on 1% tolerance resistor.
- 5. This parameter is guaranteed by design and characterization; not production tested.
- 6.  $t_{DON}/t_{DOFF}/t_R/t_F$  are defined in Figure 5 below.
- 7.  $t_{ON}=t_R+t_{DON}$ .
- 8.  $t_{OFF}=t_F+t_{DOFF}$ .

## **Timing Diagram**

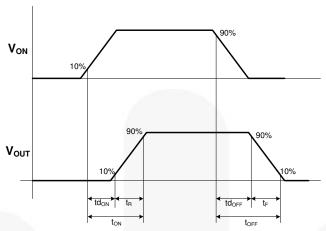


Figure 5. Timing Diagram

#### where:

$$\begin{split} &t_{DON} = Delay \ On \ Time \\ &t_{R} = V_{OUT} \ Rise \ Time \\ &t_{ON} = Turn\text{-}On \ Time \\ &t_{DOFF} = Delay \ Off \ Time \\ &t_{F} = V_{OUT} \ Fall \ Time \\ &t_{OFF} = Turn \ Off \ Time \end{split}$$

## **Operation and Application Description**

#### **Input Capacitor**

To limit the voltage drop on the input supply caused by transient inrush current when the switch turns on into discharge load capacitor; a capacitor must be placed in between the  $V_{\text{IN}}$  and GND pins. A high-value capacitor on  $C_{\text{IN}}$  can be used to reduce the voltage drop in high-current applications.

#### **Output Capacitor**

An output capacitor should be placed between the  $V_{\text{OUT}}$  and GND pins. This capacitor prevents parasitic board inductance from forcing  $V_{\text{OUT}}$  below GND when the switch is on. This capacitor also prevents reverse inrush current from creating a voltage spike that could damage the device in the case of a  $V_{\text{OUT}}$  short.

#### **Fault Reporting**

Upon the detection of an over-current, OC\_FLAGB signal the fault by activating LOW.

#### **Current Limiting**

The current limit ensures that the current through the switch does not exceed the maximum set value, while not limiting the minimum value. The current at which the part's limit is adjustable through the selection of the external resistor connected to the ISET pin. Information for selecting the resistor is found in the section below. The device acts as a constant-current source when the load draws more than the maximum value set by the device until thermal shutdown occurs. The device recovers if the die temperature drops below the threshold temperature.

## **Under-Voltage Lockout (UVLO)**

The under-voltage lockout turns the switch off if the input voltage drops below the lockout threshold. With the ON pin active, the input voltage rising above the UVLO threshold releases the lockout and enables the switch.

#### **True Reverse-Current Blocking**

The true reverse-current blocking feature protects the input source against current flow from output to input regardless of whether the load switch is on or off.

#### Thermal Shutdown

The thermal shutdown protects the die from internally or externally generated excessive temperature. During an over-temperature condition, the switch is turned off. The switch automatically turns on again if the temperature of the die drops below the threshold temperature.

## **Setting Current Limit**

The current limit is set with an external resistor connected between the  $I_{\text{SET}}$  and GND pins. The resistor is selected using Table 1. Resistor tolerance of 1% or less is recommended.

Table 1. Current Limit Settings by R<sub>SET</sub><sup>(9)</sup>

$R_{SET}\Omega$	Min. Current Limit (mA)	Typ. Current Limit (mA)	Max. Current Limit (mA)
290	3150	3500	3850
315	2925	3250	3575
340	2700	3000	3300
375	2475	2750	3025
415	2250	2500	2750
460	2025	2250	2475
520	1800	2000	2200
600	1570	1750	1920
700	1350	1500	1650
850	1125	1250	1375
1070	900	1000	1100
1200	810	900	990
1350	720	800	880
1550	630	700	770
1850	540	600	660
2300	450	500	550
2500	405	450	495
2800	360	400	440
3200	315	350	385
3800	270	300	330
4600	225	250	275
5900	180	200	220
7800	135	150	165
11900	90	100	110

Current limit threshold equation (I<sub>LIM</sub>):

$$R_{SET}(\Omega) = \left(\frac{798439 \ mV}{I_{SET} \ mA}\right)^{1.044}$$
 Or: 
$$I_{SET} \ (mA) = \frac{798439 \ mV}{R_{SET}^{0.956} \ \Omega}$$
 (1)

If current limit is not used it is OK for the ISET pin to be connected with GND, but the maximum current must be less than the maximum current capability of 3.9 A.

## **Board Layout**

For best performance, all traces should be as short as possible. To be most effective, the input and output capacitors should be placed close to the device to minimize the effect that parasitic trace inductance may have on normal and short-circuit operation. Using wide traces for VIN, VOUT, GND helps minimize parasitic electrical effects along with minimizing the case-to-ambient thermal impedance.

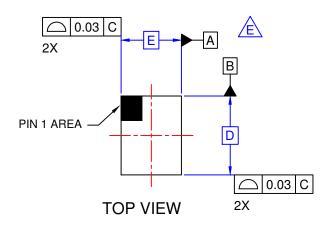
#### Note:

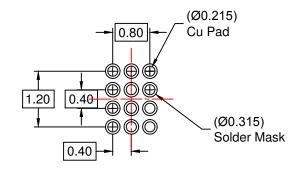
9. Table values based on 1% tolerance resistor.

The table below pertains to the Marketing outline drawing on the following page.

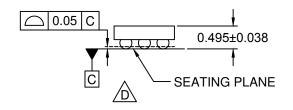
## **Product-Specific Dimensions**

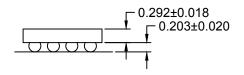
Parts	D	E	X	Υ
FPF2595UCX	1800 μm ±30 μm	1300 μm ±30 μm	250 μm	300 μm



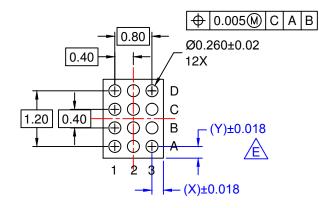


## RECOMMENDED LAND PATTERN (NSMD PAD TYPE)





SIDE VIEWS



## NOTES:

- A. NO JEDEC REGISTRATION APPLIES.
- B. DIMENSIONS ARE IN MILLIMETERS.
- C. DIMENSIONS AND TOLERANCES PER ASME Y14.5M, 2009.
- D. DATUM C IS DEFINED BY THE SPHERICAL CROWNS OF THE BALLS.
- FOR DIMENSIONS D, E, X, AND Y SEE PRODUCT DATASHEET.
- F. DRAWING FILENAME: MKT-UC012AJrev1.

**BOTTOM VIEW** 



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