Product data sheet

1. General description

Planar passivated SCR with faster switching performance and sensitive gate in a SOT223 surface mounted plastic package. This SCR with enhanced commutation performance is also designed to be interfaced directly to microcontrollers, logic integrated circuits and other low power gate trigger circuits.

2. Features and benefits

- Fast commutation performance for higher frequency operation
- Full wave rectified AC applications
- Sensitive gate
- · Direct triggering from microcontrollers, low power drivers and logic ICs

3. Applications

- · Earth leakage circuit breakers (ELCB/GFI)
- · Ignition circuits (gas appliances, small engines and HID lighting)

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V_{RRM}	repetitive peak reverse voltage		-	-	600	V
$I_{T(AV)}$	average on-state current	half sine wave; T _{sp} ≤ 112 °C; <u>Fig. 1</u>	-	-	0.63	Α
I _{T(RMS)}	RMS on-state current	half sine wave; $T_{sp} \le 112 ^{\circ}\text{C}$; Fig. 2; Fig. 3	-	-	1	Α
I _{TSM}	non-repetitive peak on- state current	half sine wave; $T_{j(init)}$ = 25 °C; t_p = 10 ms; Fig. 4; Fig. 5	-	-	8	Α
		half sine wave; $T_{j(init)}$ = 25 °C; t_p = 8.3 ms	-	-	9	Α
Tj	junction temperature		-	-	125	°C
Static charact	teristics					,
I _{GT}	gate trigger current	V_D = 12 V; I_T = 10 mA; T_j = 25 °C; Fig. 9	70	200	450	μΑ
Dynamic char	racteristics					
dV _D /dt	rate of rise of off-state voltage	V_{DM} = 402 V; T_j = 125 °C; R_{GK} = 1 kΩ; $(V_{DM}$ = 67% of V_{DRM}); exponential waveform; Fig. 11	350	800	-	V/µs

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Symbol	Parameter	Conditions	Min	Тур	Max	Unit
		V_{DM} = 402 V; T_j = 125 °C; (V_{DM} = 67% of V_{DRM}); exponential waveform; gate open circuit; Fig. 11	-	25	-	V/µs

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	K	cathode	4	А - К
2	Α	anode		G sym037
3	G	gate		symosi
4	mb	mb; connected to anode	⊟1 ⊟2 ⊟3 SC-73 (SOT223)	

6. Ordering information

Table 3. Ordering information

Type number	Package	ge					
	Name	Description	Version				
BT168GWF	SC-73	plastic surface-mounted package with increased heatsink; 4 leads	SOT223				

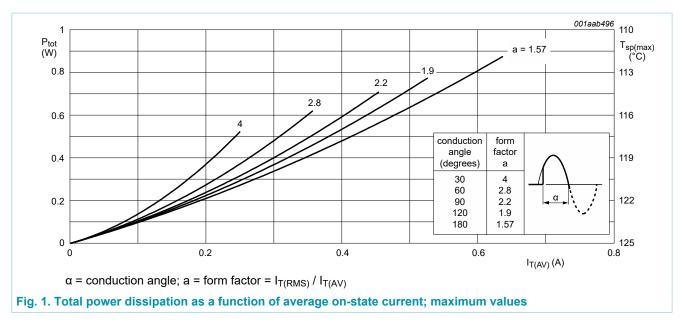
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7. Limiting values

Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
V_{DRM}	repetitive peak off-state voltage		-	600	V
V_{RRM}	repetitive peak reverse voltage		-	600	V
I _{T(AV)}	average on-state current	half sine wave; T _{sp} ≤ 112 °C; <u>Fig. 1</u>	-	0.63	Α
I _{T(RMS)}	RMS on-state current	half sine wave; T _{sp} ≤ 112 °C; <u>Fig. 2</u> ; <u>Fig. 3</u>	-	1	Α
I _{TSM}	non-repetitive peak on- state current	half sine wave; $T_{j(init)} = 25 \text{ °C}$; $t_p = 10 \text{ ms}$; Fig. 4; Fig. 5	-	8	Α
		half sine wave; T _{j(init)} = 25 °C; t _p = 8.3 ms	-	9	Α
I ² t	I ² t for fusing	t _p = 10 ms; SIN	-	0.32	A²s
dl _T /dt	rate of rise of on-state current	$I_T = 2 \text{ A}$; $I_G = 10 \text{ mA}$; $dI_G/dt = 100 \text{ mA/}\mu\text{s}$	-	50	A/µs
I _{GM}	peak gate current		-	1	Α
V_{RGM}	peak reverse gate voltage		-	5	V
P _{GM}	peak gate power		-	2	W
P _{G(AV)}	average gate power	over any 20 ms period	-	0.1	W
T _{stg}	storage temperature		-40	150	°C
T _j	junction temperature		-	125	°C



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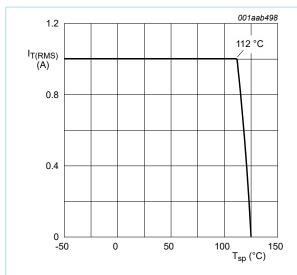


Fig. 2. RMS on-state current as a function of solder point temperature; maximum values

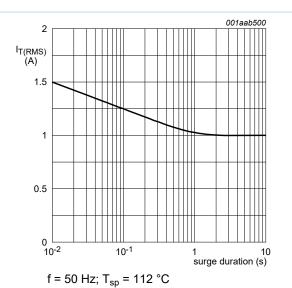


Fig. 3. RMS on-state current as a function of surge duration; maximum values

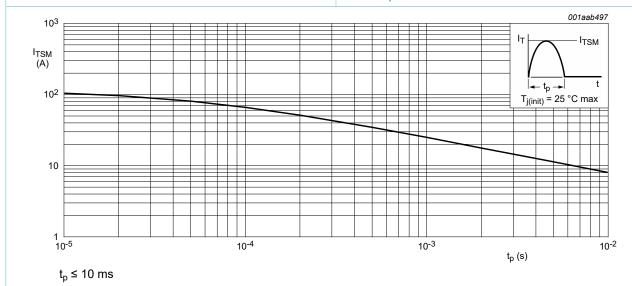


Fig. 4. Non-repetitive peak on-state current as a function of pulse width for sinusoidal currents; maximum values

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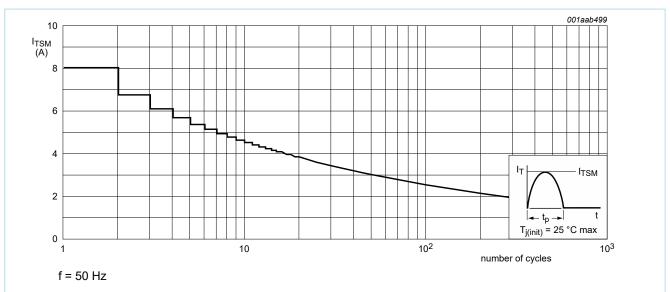


Fig. 5. Non-repetitive peak on-state current as a function of the number of sinusoidal current cycles; maximum values

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8. Thermal characteristics

Table 5. Thermal characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
R _{th(j-sp)}	thermal resistance from junction to solder point	Fig. 6	-	-	15	K/W
R _{th(j-a)}	thermal resistance from junction to	printed-circuit board mounted; minimum footprint; Fig. 7	-	156	-	K/W
	ambient free air	printed-circuit board mounted; pad area; Fig. 8	-	70	-	K/W

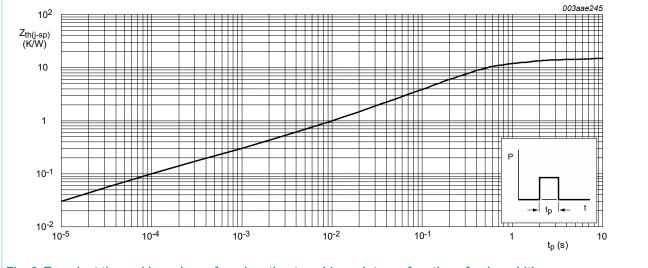
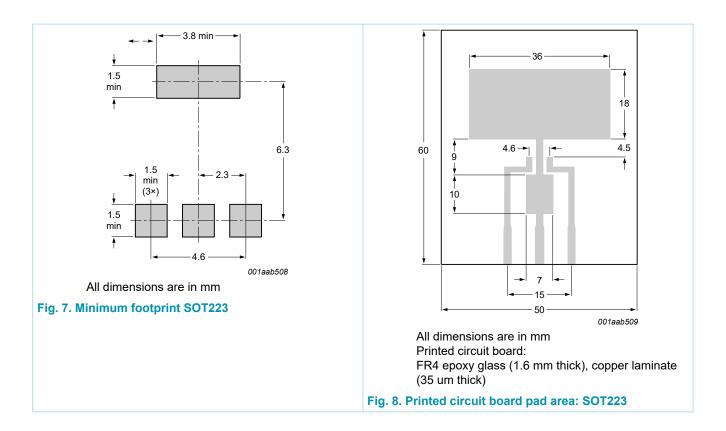


Fig. 6. Transient thermal impedance from junction to solder point as a function of pulse width

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9. Characteristics

Table 6. Characteristics

Table 6. Clia	racteristics					
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Static chara	acteristics					
I _{GT}	gate trigger current	$V_D = 12 \text{ V}; I_T = 10 \text{ mA}; T_j = 25 ^{\circ}\text{C};$ Fig. 9	70	200	450	μΑ
IL	latching current	V_D = 12 V; I_G = 0.5 mA; T_j = 25 °C; $R_{GK(ext)}$ = 1 k Ω	3	7.5	13	mA
I _H	holding current	$V_D = 12 \text{ V}; T_j = 25 \text{ °C}; R_{GK(ext)} = 1 \text{ k}\Omega$	0.5	4.1	10	mA
V _T	on-state voltage	I _T = 1.2 A; T _j = 25 °C; <u>Fig. 10</u>	-	1.35	1.7	V
V_{GT}	gate trigger voltage	V _D = 12 V; I _T = 10 mA; T _j = 25 °C	-	0.5	8.0	V
		V _D = 600 V; I _T = 10 mA; T _j = 125 °C	0.2	0.3	-	V
I _D	off-state current	$V_D = 600 \text{ V}; R_{GK(ext)} = 1 \text{ k}\Omega; T_j = 125 \text{ °C}$	-	0.05	0.1	mA
I _R	reverse current	$V_R = 600 \text{ V}; T_j = 125 \text{ °C}; R_{GK(ext)} = 1 \text{ k}\Omega$	-	0.05	0.1	mA
Dynamic ch	aracteristics					
dV _D /dt	rate of rise of off-state voltage	V_{DM} = 402 V; T_j = 125 °C; R_{GK} = 1 kΩ; (V_{DM} = 67% of V_{DRM}); exponential waveform; Fig. 11	350	800	-	V/µs
		V_{DM} = 402 V; T_j = 125 °C; (V_{DM} = 67% of V_{DRM}); exponential waveform; gate open circuit; Fig. 11	-	25	-	V/µs

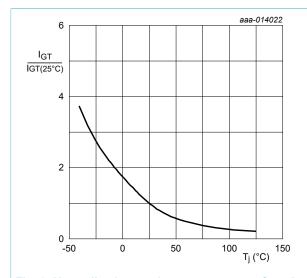


Fig. 9. Normalized gate trigger current as a function of junction temperature

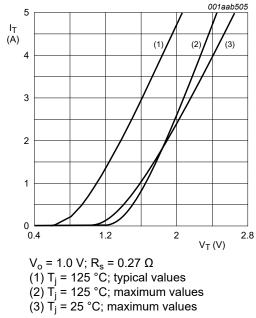
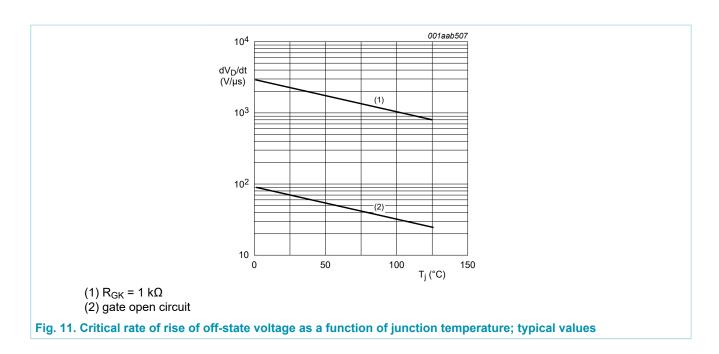


Fig. 10. On-state current as a function of on-state voltage

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10. Package outline

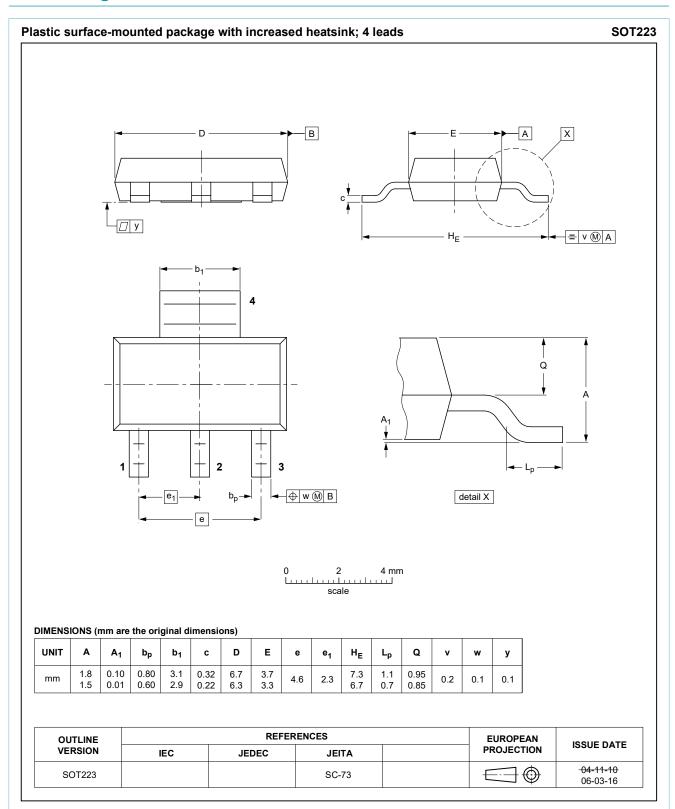
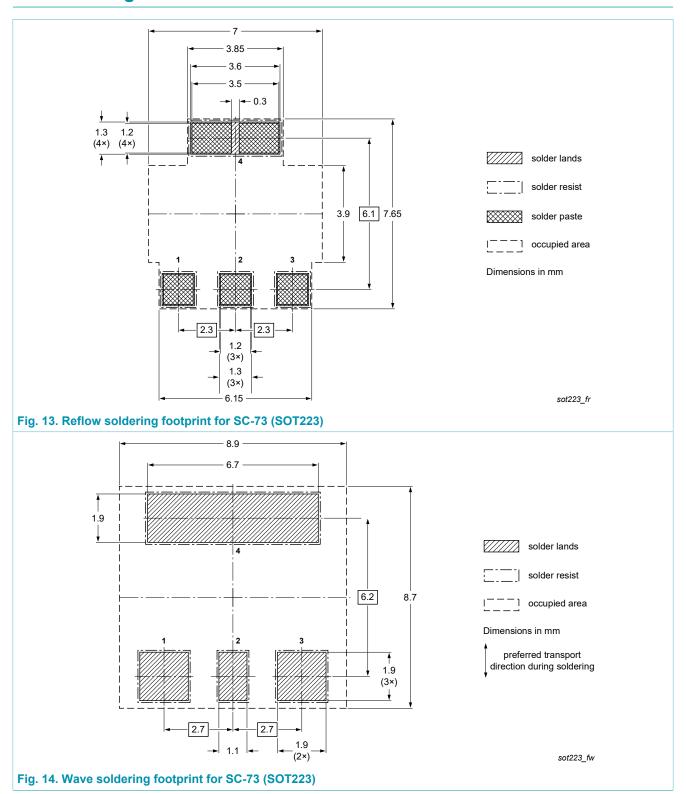


Fig. 12. Package outline SC-73 (SOT223)

11. Soldering



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12. Legal information

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Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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