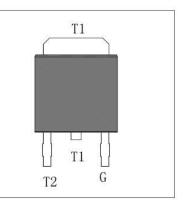


## BT151S-650L

### APPLICATIONS

Mesa glass passivation technology; Have high blocking voltage and high temperature stability cleaner; Electric tools such as motor speed controller; Solid state relay; Heating controller (temperature); Other phase control circuit Minimum Lot-to-Lot variations for robust device performance and reliable operation



### ABSOLUTE MAXIMUM RATINGS(Ta=25°C)

| SYMBOL             | PARAMETER  | MIN       | UNIT             |
|--------------------|--|-----------|------------------|
| V <sub>DRM</sub>   | Repetitive peak off-state voltage                            | 650       | V                |
| V <sub>RRM</sub>   | Repetitive peak reverse voltage                              | 650       | V                |
| It(av)             | On-state current Tc=80℃                                      | 7.5       | А                |
| I <sub>TSM</sub>   | Surge non-repetitive on-state current ,T=10ms                | 120       | А                |
| P <sub>G(AV)</sub> | Average gate power   | 5         | W                |
| di/dt              | Repetitive rate of rise of on-state current after triggering | 5         | A/us             |
| l <sup>2</sup> t   | $I^{2}t$ for fusing t = 10 ms                                | 72        | A <sup>2</sup> S |
| I <sub>GM</sub>    | Peak gate current tp=20us ,Tj=125℃                           | 2         | A                |
| Tj                 | Operating Junction temperature                               | -40 ~+125 | °C               |
| T <sub>stg</sub>   | Storage temperature  | -40 ~+150 | °C               |

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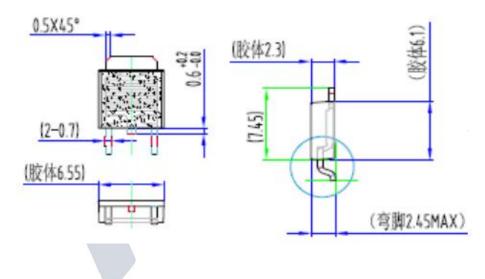
### **INCHANGE SEMICONDUCTOR**

# **isc** Thyristors

# BT151S-650L

### ELECTRICAL CHARACTERISTICS (TC=25°C unless otherwise specified)

| SYMBOL               | PARAMETER                                    | CONDITIONS                                   | MIN | TYP. | MAX  | UNIT |
|----------------------|--|--|-----|------|------|------|
| I <sub>RRM</sub>     | Repetitive peak reverse current              | V <sub>RRM</sub> =650V, Тј=125℃              |     |      | 5    | mA   |
| I <sub>DRM</sub>     | Repetitive peak off-state current            | V <sub>DRM</sub> =650V, Тј=125℃              |     |      | 5    | mA   |
| V <sub>TM</sub>      | On-state voltage                             | I <sub>TM</sub> = 23A                        |     |      | 1.75 | V    |
| I <sub>GT</sub>      | Gate-trigger current                         | $V_D$ =12V; R <sub>L</sub> =100 $\Omega$     |     |      | 5    | mA   |
| V <sub>GT</sub>      | Gate-trigger voltage                         | $V_{D}$ =12V; R <sub>L</sub> =100 $\Omega$   |     |      | 1.5  | V    |
| Iн                   | Holding current                              | I⊤=0.5A                                      |     |      | 20   | mA   |
| dv/dt                | Critical rate of rise of off-state voltage   | V <sub>D</sub> =0.67V <sub>DRM</sub> Tj=125℃ | 200 |      |      | V/us |
| R <sub>th(j-c)</sub> | Thermal resistance junction to mounting base | in free air                                  |     | 1.75 |      | °C/W |



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