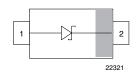


## Vishay Semiconductors

# **Small Signal Schottky Diode**





#### **LINKS TO ADDITIONAL RESOURCES**



#### **MECHANICAL DATA**

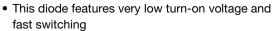
Case: SOD-523

Weight: approx. 1.4 mg

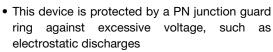
Molding compound flammability rating: UL 94 V-0 **Terminals:** high temperature soldering guaranteed:

260 °C/4 x 10 s at terminals **Packaging codes / options:** 08/8K per 7" reel (8 mm tape)

#### **FEATURES**









• AEC-Q101 qualified available

• Space saving SOD-523 package

ROHS COMPLIANT HALOGEN FREE GREEN

Base P/N-G3 - RoHS-compliant, commercial grade

• Base P/N-HG3 - RoHS-compliant, AEC-Q101 qualified

 Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912">www.vishay.com/doc?99912</a>

PARTS TABLE						
PART	ORDERING CODE	AEC-Q101 QUALIFIED	CIRCUIT CONFIGURATION	TYPE MARKING	REMARKS	
BAT54-02V	BAT54-02V-G3-08	no	Single	·\/	Tape and reel	
	BAT54-02V-HG3-08	yes	Sirigle	. v	rape and reei	

<b>ABSOLUTE MAXIMUM RATINGS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT		
Repetitive peak reverse voltage = working peak reverse voltage		V <sub>RRM</sub>	30	V		
Forward continuous current		I <sub>F</sub>	200	mA		
Repetitive peak forward current		I <sub>FRM</sub>	300	mA		
Surge forward current	t <sub>p</sub> = 10 ms square wave, T <sub>j</sub> = 25 °C prior to surge	I <sub>FSM</sub>	600	mA		
Power dissipation		P <sub>tot</sub>	150	mW		

THERMAL CHARACTERISTICS (T <sub>amb</sub> = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT		
Thermal resistance junction to ambient air		R <sub>thJA</sub>	680	K/W		
Thermal resistance junction to lead		R <sub>thJL</sub>	480	K/W		
Junction temperature		Tj	125	°C		
Operating temperature range		T <sub>op</sub>	-55 to +125	°C		
Storage temperature range		T <sub>stg</sub>	-55 to +150	°C		

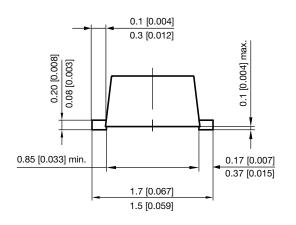


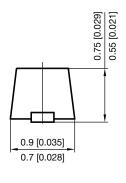
### www.vishay.com

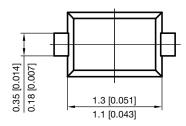
# Vishay Semiconductors

<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Reverse breakdown voltage	100 μA pulses	V <sub>(BR)</sub>	30			V
Leakage current	Pulse test $t_p < 300 \ \mu s$ , $\delta < 2 \ \%$ at $V_R = 25 \ V$	I <sub>R</sub>			2	μΑ
	$I_F$ = 0.1mA, $t_p$ < 300 $\mu$ s, $\delta$ < 2 %	V <sub>F</sub>			240	mV
	$I_F$ = 1 mA, $t_p$ < 300 $\mu$ s, $\delta$ < 2 %	V <sub>F</sub>			320	mV
Forward voltage	$I_F$ = 10 mA, $t_p$ < 300 $\mu$ s, $\delta$ < 2 %	V <sub>F</sub>			400	mV
	$I_F$ = 30 mA, $t_p$ < 300 $\mu$ s, $\delta$ < 2 %	V <sub>F</sub>			500	mV
	$I_F$ = 100 mA, $t_p$ < 300 $\mu$ s, $\delta$ < 2 %	V <sub>F</sub>			800	mV
Diode capacitance	V <sub>R</sub> = 1 V, f = 1 MHz	C <sub>D</sub>			10	pF
Reverse recovery time	$I_F$ = 10 mA, $I_R$ = 10 mA, $I_R$ = 1 mA, $R_L$ = 100 $\Omega$	t <sub>rr</sub>			5	ns

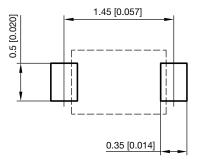
### PACKAGE DIMENSIONS in millimeters [inches]: SOD-523







Footprint recommendation:



Document no.: S8-V-3880.02-003 (4) Created - Date: 04. April 2017 Rev. 4 - Date: 03. Aug. 2020

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Vishay

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